

U8			
HA-16 N5	AH16	ADS#	G1 HADS-
HA-15 N4	AH15	AP0#	AC1
HA-14 N2	AH14	API#	V5 X
HA-13 M1	AH13	BINIT#	AA3 X
HA-12 N1	AH12	BNR#	G2 X HBNR-
HA-11 M4	AH11		L25
HA-10 M3	AH10		K28 X
HA-9 L2	AH9	DP#3	K29 X
HA-8 M6	AH8	DP#2	J26 X
HA-7 L3	AH7	DP#1	
HA-6 K1	AH6	DP#0	
HA-5 L6	AH5		U6 TESTHI8
HA-4 K4	AH4	TESTHI9	W4 TESTHI9
HA-3 K2	AH3	TESTHI10	Y3 TESTHI10
HADSTB-0 L5	ADSTB0#	BR#0	H6 HBREQ-0
HREQ-4 H3	REQ#4		
HREQ-3 J3	REQ#3	BPRI#	D2 HBPRI-
HREQ-2 J4	REQ#2		
HREQ-1 K5	REQ#1	DBSY#	H5 HDBSY-
HREQ-0 J1	REQ#0	DEFER#	E2 HDEFER-
		DRDY#	H2 HDRDY-
		HITM#	F3 HHIT-
			E3 HHITM-
AB1 X1	AH35		
X1	AH34		
X2	AH33		
X4	AH32	IER#	AC3 X
HA-31 T5	AH31	INIT#	W5 HINIT-
HA-29 W1	AH29		
HA-28 R6	AH28	LOCK#	G4 HLOCK-
HA-27 V2	AH27		
HA-26 T4	AH26	MCERR#	V6 X
HA-25 U3	AH25		
HA-24 P6	AH24		
HA-23 U1	AH23	RESET#	AB25 CPURST-
HA-22 T2	AH22	RS#2	F4 HRS-2
HA-21 R3	AH21	RS#1	G5 HRS-1
HA-20 P4	AH20	RS#0	F1 HRS-0
HA-19 P3	AH19	RSP#	E2 X
HA-18 R2	AH18	TRDY#	J6 HTRDY-
HA-17 T1	AH17		
HADSTB-1 R5	ADSTB1#		

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U8B			
HD-15 D25	DH15	D#47	T23 HD-47
HD-14 J21	DH14	D#46	T22 HD-46
HD-13 D23	DH13	D#45	T25 HD-45
HD-12 C26	DH12	D#44	T26 HD-44
HD-11 H21	DH11	D#43	R24 HD-43
HD-10 G22	DH10	D#42	E25 HD-42
HD-9 B25	DH9	D#41	P24 HD-41
HD-8 C24	DH8	D#40	N25 HD-39
HD-7 C23	DH7	D#39	N26 HD-38
HD-6 B24	DH6	D#38	M26 HD-37
HD-5 D22	DH5	D#37	N23 HD-36
HD-4 C21	DH4	D#36	M24 HD-35
HD-3 A25	DH3	D#35	E21 HD-34
HD-2 A23	DH2	D#34	N22 HD-33
HD-1 B22	DH1	D#33	M23 HD-32
HD-0 B21	DH0	D#32	P26 HD-31
HDINV-0 E21	DBI0#	D#31	R22 HDSTBN-2
HDSTBN-0 E22	DSTBN#0	D#30	E23 HDSTBP-2
HDSTBP-0 F21	DSTBN#0	D#29	
		D#28	
		D#27	
		D#26	
		D#25	
		D#24	
		D#23	
		D#22	
		D#21	
		D#20	
		D#19	
		D#18	
		D#17	
		D#16	
HDINV-1 G25	DBI#1	D#15	AA24 HD-63
HDSTBN-1 K24	DSTBN#1	D#14	AA22 HD-62
HDSTBP-1 J23	DSTBP#1	D#13	AA25 HD-61
		D#12	Y21 HD-60
		D#11	Y24 HD-59
		D#10	Y23 HD-58
		D#9	W25 HD-57
		D#8	Y26 HD-56
		D#7	W26 HD-55
		D#6	V24 HD-54
		D#5	V22 HD-53
		D#4	U21 HD-52
		D#3	V25 HD-51
		D#2	U23 HD-50
		D#1	U24 HD-49
		D#0	U26 HD-48
		D#48	
		D#47	
		D#46	
		D#45	
		D#44	
		D#43	
		D#42	
		D#41	
		D#40	
		D#39	
		D#38	
		D#37	
		D#36	
		D#35	
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		D#32	
		D#31	
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		D#16	
		D#15	
		D#14	
		D#13	
		D#12	
		D#11	
		D#10	
		D#9	
		D#8	
		D#7	
		D#6	
		D#5	
		D#4	
		D#3	
		D#2	
		D#1	
		D#0	

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U8A			
CPUHCLK AF22	BCLK0	BSEL0	AD6 BSEL0
CPUHCLK AF23	BCLK1	BSEL1	AD5 BSEL1
AC26	ITP_CLK0	TESTHI11	A6 TESTHI11
AD26	ITP_CLK1	COMP0	L24 HCOMP0
		COMP1	F1 HCOMP1
A20M- C6	A20M#	AB4 BPM-5	
FERR- B6	FERR#	AB5 BPM-4	
IGNNE- B2	IGNNE#	AC6 BPM-1	
		AC6 BPM-0	
INTR D1	LINT0		
NMI E5	LINT1		
SMI- B5	SMI#		
STPCLK- Y4	STPCLK#		
		TESTHI0	AD24 TESTHI0
		DBR#	AE25 DBRESET-
		TESTHI12	AD25 TESTHI12
VID4 AE1	VID4	AA6	
VID3 AE2	VID3	AA7	
VID2 AE3	VID2	AA8	
VID1 AE4	VID1	AA9	
VID0 AE5	VID0	F20	GTLREF1
VCCPVID AF2	VCC	TESTHI5	AC23
AF4	VCCVID	TESTHI4	AC24
AF3	RSVD	TESTHI3	AC20
AF2	VCCA	TESTHI2	AC21
VCCSENSE A5	VCCSENSE	TESTHI1	AB22
VCCIOPLL AE23	VCCIOPLL	TESTHI0	AA20
AD22	VSSA		
VSSSENSE A4	VSSSENSE		
		SKTOCC#	AF26 CPUMISS
AE21	RSVD	TESTHI1	AA2 TESTHI1
X7	RSVD	AB23	PWRGOOD
	RSVD	C3	PROCHOT-
B3	THRMDC	AB26	CPUSLP-
AGND SIO C4	THRMDC		
THERTRP- A2	THERMTRIP#		
		D4	TCK
VID5 AD3	RSVD	C1	TDI
AF25	RSVD	D5	TDO
X22	RSVD	F7	TMS
AF24	RSVD	E6	TRST-
B1	NC_KEY_B1		

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U8C			
H011	HOLE1-1	H031	HOLE3-1
H012	HOLE1-2	H032	HOLE3-2
H013	HOLE1-3	H033	HOLE3-3
H014	HOLE1-4	H034	HOLE3-4
H015	HOLE1-5	H035	HOLE3-5
H016	HOLE1-6	H036	HOLE3-6
H017	HOLE1-7	H037	HOLE3-7
H018	HOLE1-8	H038	HOLE3-8
H021	HOLE2-1	H041	HOLE4-1
H022	HOLE2-2	H042	HOLE4-2
H023	HOLE2-3	H043	HOLE4-3
H024	HOLE2-4	H044	HOLE4-4
H025	HOLE2-5	H045	HOLE4-5
H026	HOLE2-6	H046	HOLE4-6
H027	HOLE2-7	H047	HOLE4-7
H028	HOLE2-8	H048	HOLE4-8

NWD478-1

HA-[3..31]	HA-[3..31]	11
HADSTB-[0..1]	HADSTB-[0..1]	11
HD-[0..63]	HD-[0..63]	11
HDINV-[0..3]	HDINV-[0..3]	11
HDSTBN-[0..3]	HDSTBN-[0..3]	11
HDSTBP-[0..3]	HDSTBP-[0..3]	11
HRS-[0..2]	HRS-[0..2]	11
HREQ-[0..4]	HREQ-[0..4]	11

HADS-	HADS-	11
HBREQ-0	HBREQ-0	3,11
HBPRI-	HBPRI-	11
HBNR-	HBNR-	11
HLOCK-	HLOCK-	11
HHIT-	HHIT-	11
HHITM-	HHITM-	11
HDEFER-	HDEFER-	11
HTRDY-	HTRDY-	11
HDRDY-	HDRDY-	11
HDBSY-	HDBSY-	11
PWRGOOD	PWRGOOD	3,19
CPURST-	CPURST-	3

A20M-	A20M-	19
IGNNE-	IGNNE-	19
NMI	NMI	19
INTR	INTR	19
HINIT-	HINIT-	19
SMI-	SMI-	19
STPCLK-	STPCLK-	19
CPUSLP-	CPUSLP-	19
FERR-	FERR-	19
THERTRP-	THERTRP-	19

BSEL0	BSEL0	3,5
BSEL1	BSEL1	3,5
TCK	TCK	3
TDI	TDI	3
TDO	TDO	3
TMS	TMS	3
DBRESET-	DBRESET-	3
TRST-	TRST-	3
TESTHI0	TESTHI0	3
TESTHI1	TESTHI1	3
TESTHI2	TESTHI2	3
TESTHI3	TESTHI3	3
TESTHI4	TESTHI4	3
TESTHI5	TESTHI5	3
TESTHI6	TESTHI6	3
TESTHI7	TESTHI7	3
TESTHI8	TESTHI8	3
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TESTHI90	TESTHI90	3
TESTHI91	TESTHI91	3
TESTHI92	TESTHI92	3
TESTHI93	TESTHI93	3
TESTHI94	TESTHI94	3
TESTHI95	TESTHI95	3
TESTHI96	TESTHI96	3
TESTHI97	TESTHI97	3
TESTHI98	TESTHI98	3
TESTHI99	TESTHI99	3

GTLREF1	GTLREF1	3
CPUINT	CPUINT	38
AGND SIO	AGND SIO	37,38
CPUHCLK	CPUHCLK	4
CPUHCLK-	CPUHCLK-	4

VID[0..5]	VID[0..5]	3,5,7
H VCCA	H VCCA	3
H VSSA	H VSSA	3
VCCIOPLL	VCCIOPLL	3

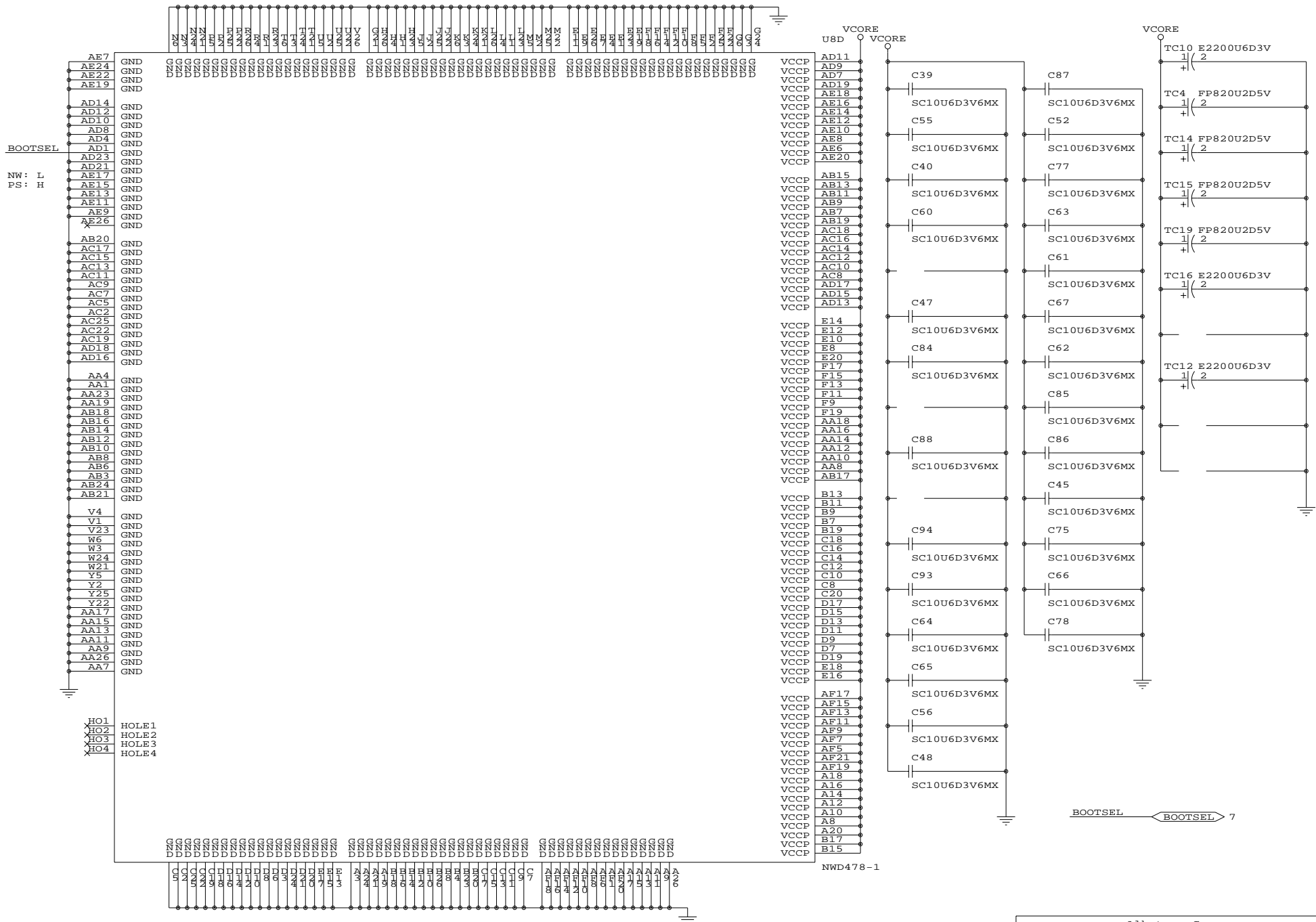
VCCPVID	VCCPVID	6
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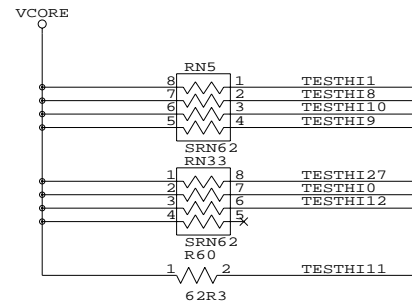
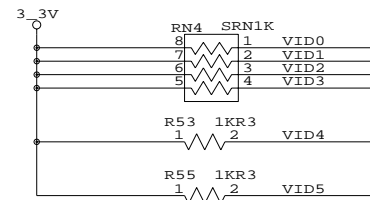
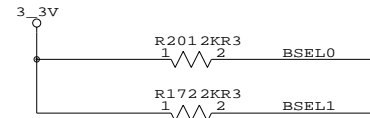
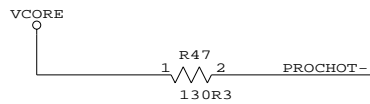
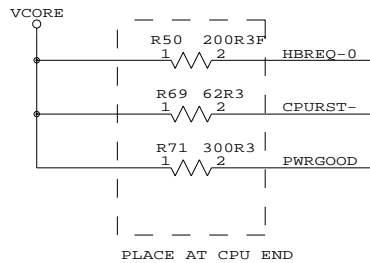
VCCVIDGD	VCCVIDGD	6
PROCHOT-	PROCHOT-	3,11

CPUMISS	CPUMISS	5
VCCSENSE	VCCSENSE	5
VSSSENSE	VSSSENSE	5

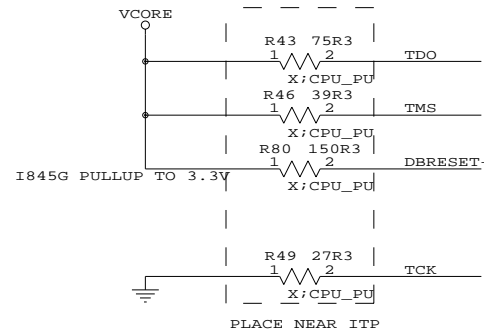
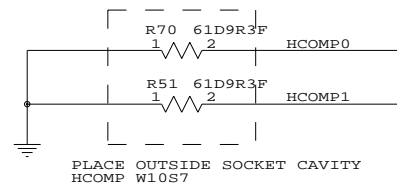
VCCVIDGD	VCCVIDGD	6
PROCHOT-	PROCHOT-	3,11

CPUMISS	CPUMISS	5
VCCSENSE	VCCSENSE	5
VSSSENSE	VSSSENSE	

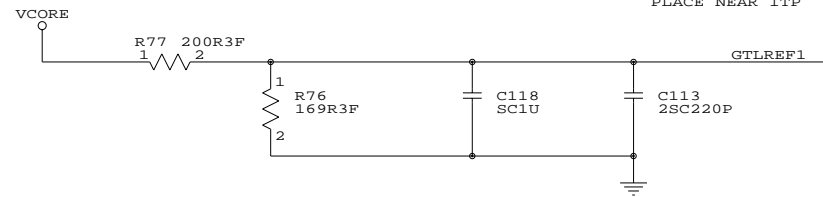




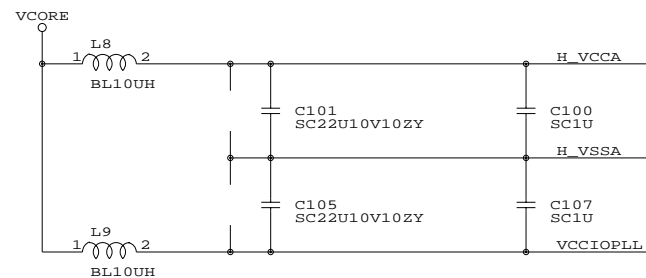
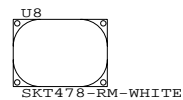
REF. PRESCOOT EDS 0.5



I845G PULLUP TO 3.3V



EACH PIN WITH A CAP 220P
AS CLOSE TO PIN AS POSSIBLE
W7S10 < 1.5"

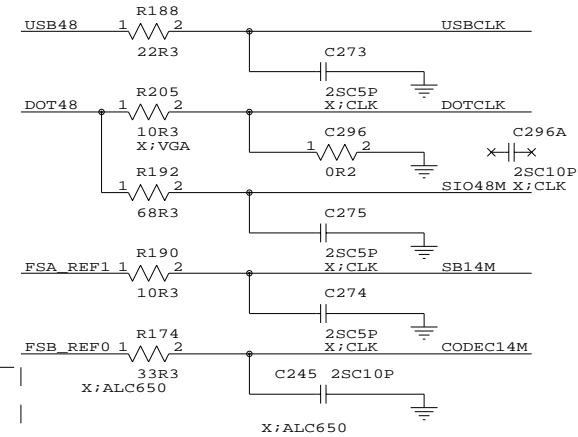
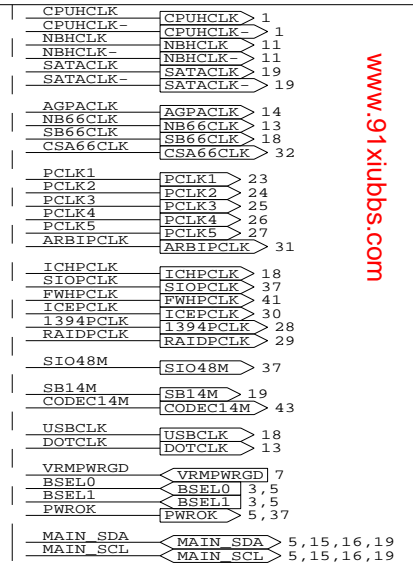
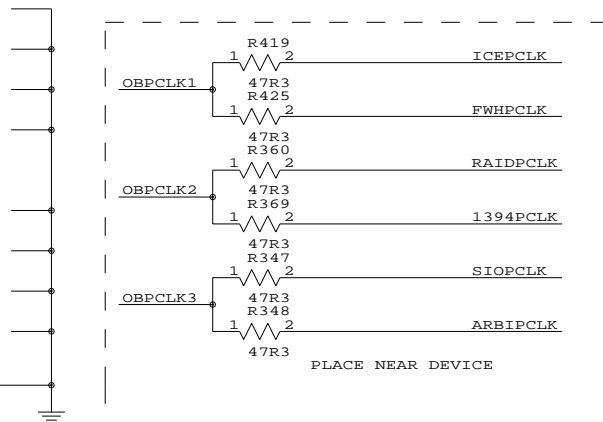
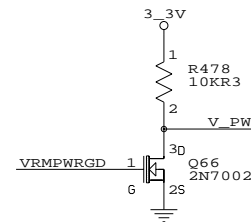
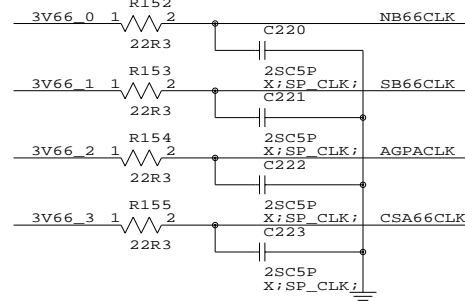
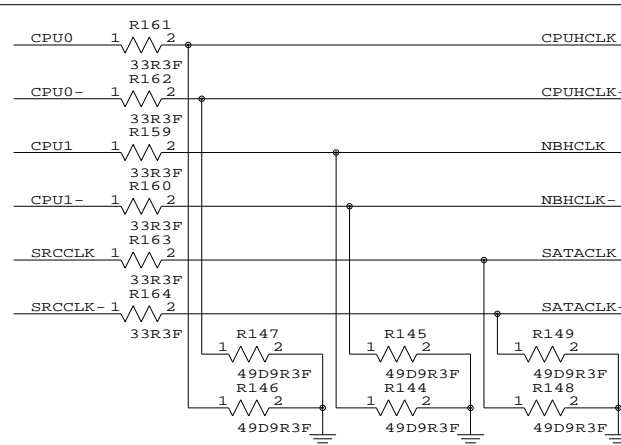
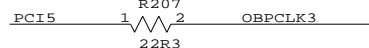
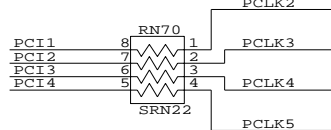
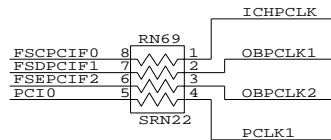
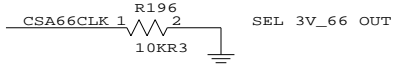
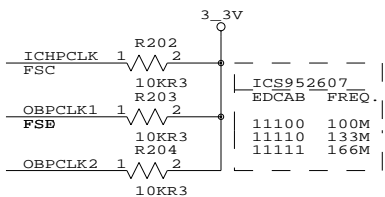
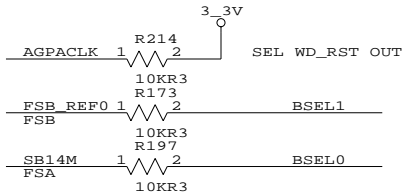
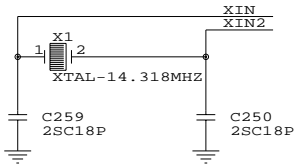
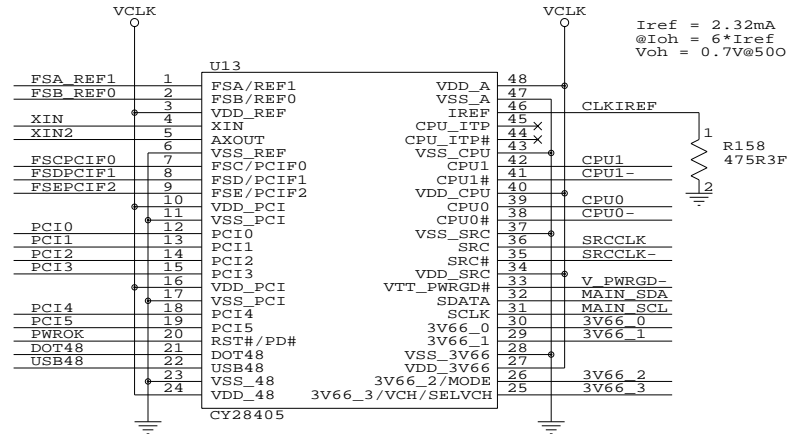
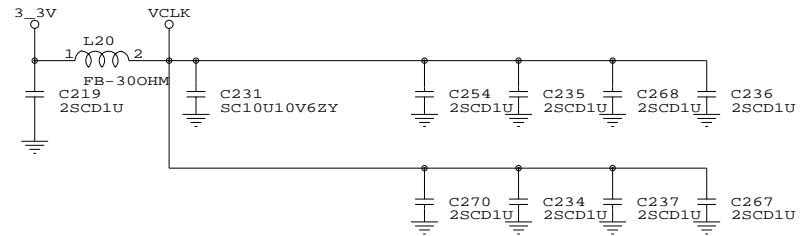


THE TRACE (FROM Vcc TO CAPACITOR) IMPEDANCE MUST LARGER THAN 0.35OHM AND LESS THAN 10HM

TESTHI[8..12]	TESTHI[8..12]	1
HCOMP[0..1]	HCOMP[0..1]	1
BPM-[0..1]	BPM-[0..1]	1,3
BPM-[4..5]	BPM-[4..5]	1,3
TESTHI0	TESTHI0	1
TESTHI1	TESTHI1	1
TESTHI27	TESTHI27	1
HBREQ-0	HBREQ-0	1,11
BSEL0	BSEL0	1,4,5
BSEL1	BSEL1	1,4,5
PWRGOOD	PWRGOOD	1,19
CPURST-	CPURST-	1,11
TCK	TCK	1
TDI	TDI	1
TDO	TDO	1
TMS	TMS	1
DBREEST-	DBREEST-	1
TRST-	TRST-	1
GTLREF1	GTLREF1	1,11
H_VCCA	H_VCCA	1
H_VSSA	H_VSSA	1
VCCIOPLL	VCCIOPLL	1
PROCHOT-	PROCHOT-	1
VID[0..5]	VID[0..5]	1

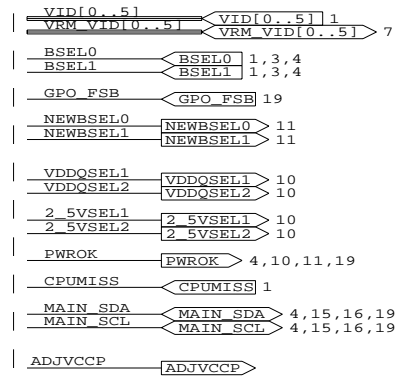
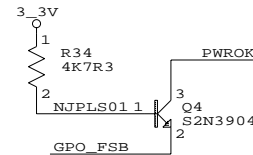
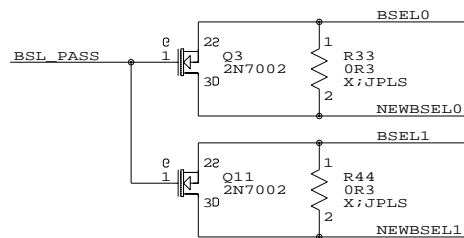
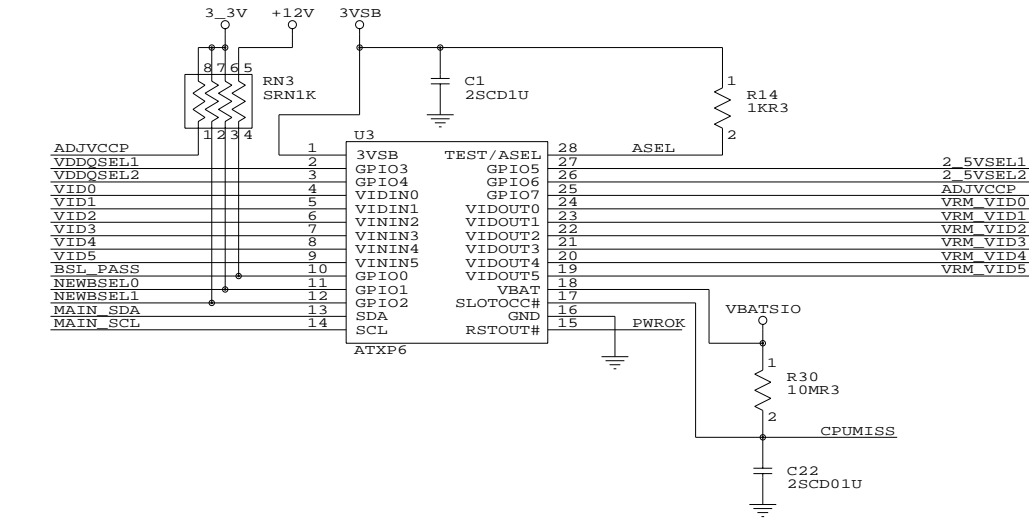
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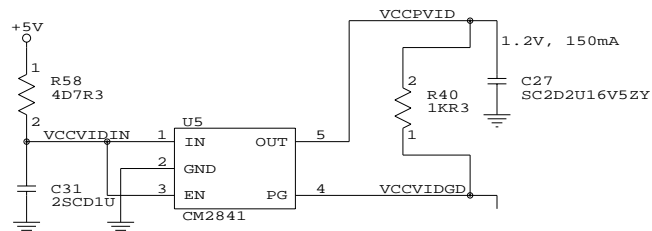
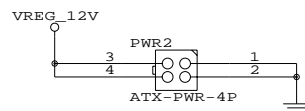
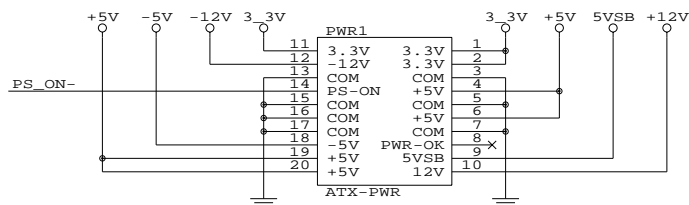
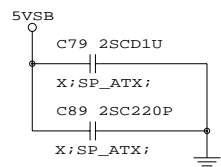
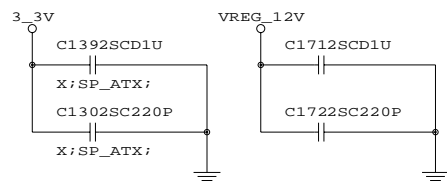
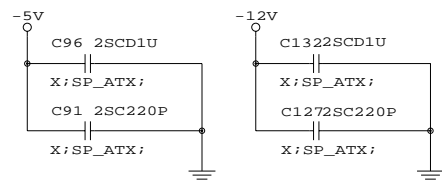
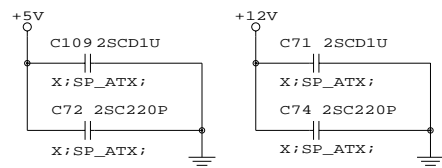
Albatron Corp.		
Title		
Northwood PULL UP/DOWN		
Size	Document Number	REV
B	PX865PE PROII	1.0
Date:	March 28, 2003	Sheet 3 of 46



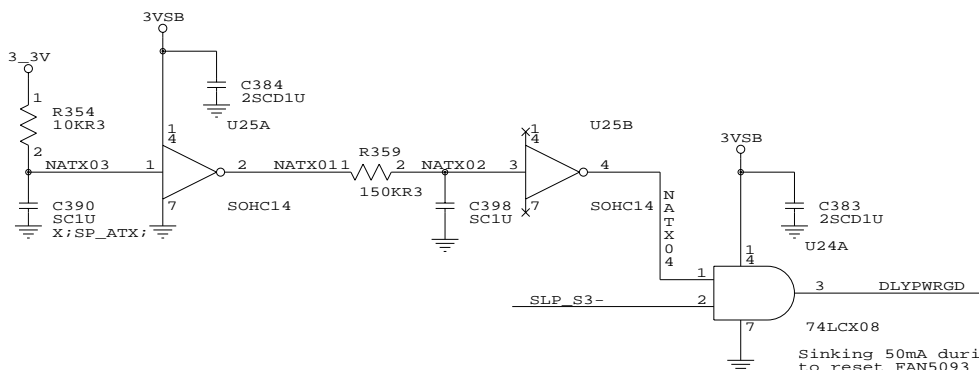
MULTSEL1:0	WDRST-
00: 4 x Iref	PULL UP TO 3VSB WITH 1KO
01: 5 x Iref	AT MULTIFUN.SCH
10: 6 x Iref	PULL DOWN WITH 22KO
11: 7 x Iref	AT ICH4_PUD.SCH

Albatron Corp.		
Title		
CLOCK		
Size	Document Number	REV
B	PX865PE PROII	1.0
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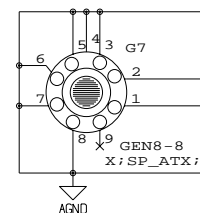
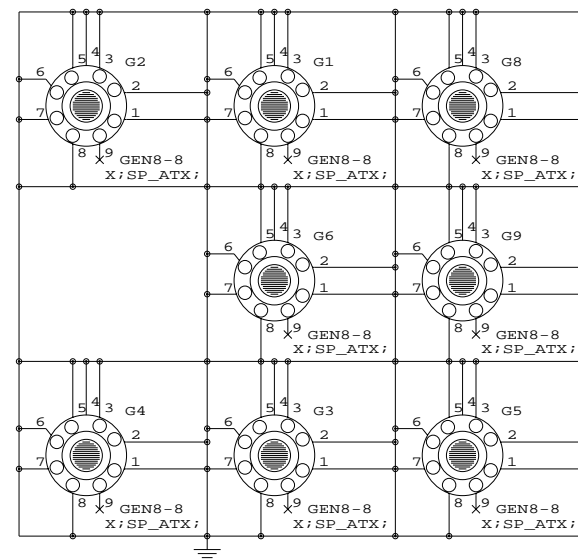
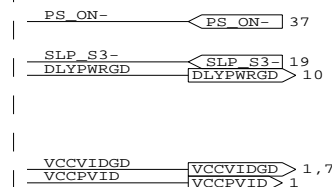




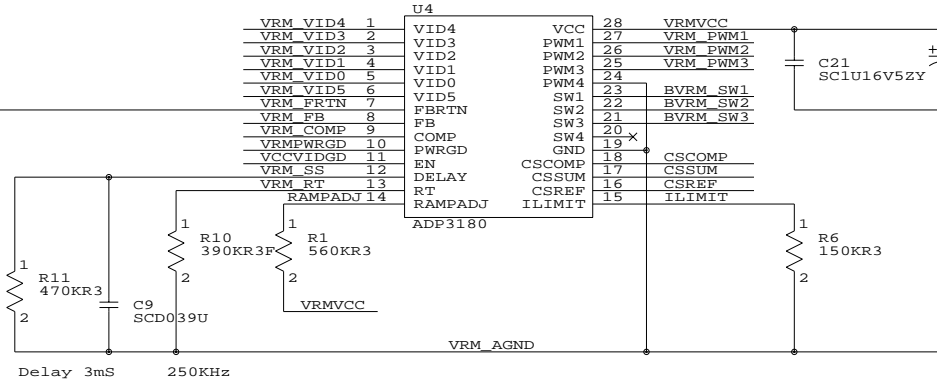
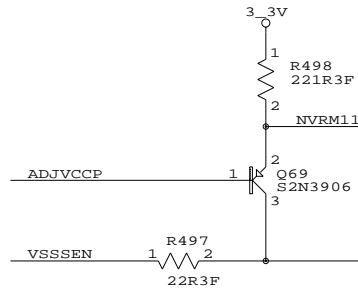
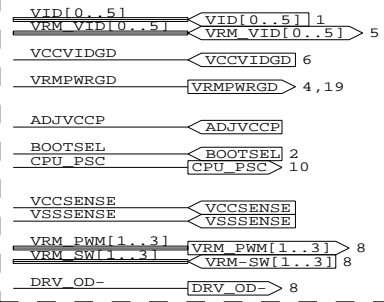
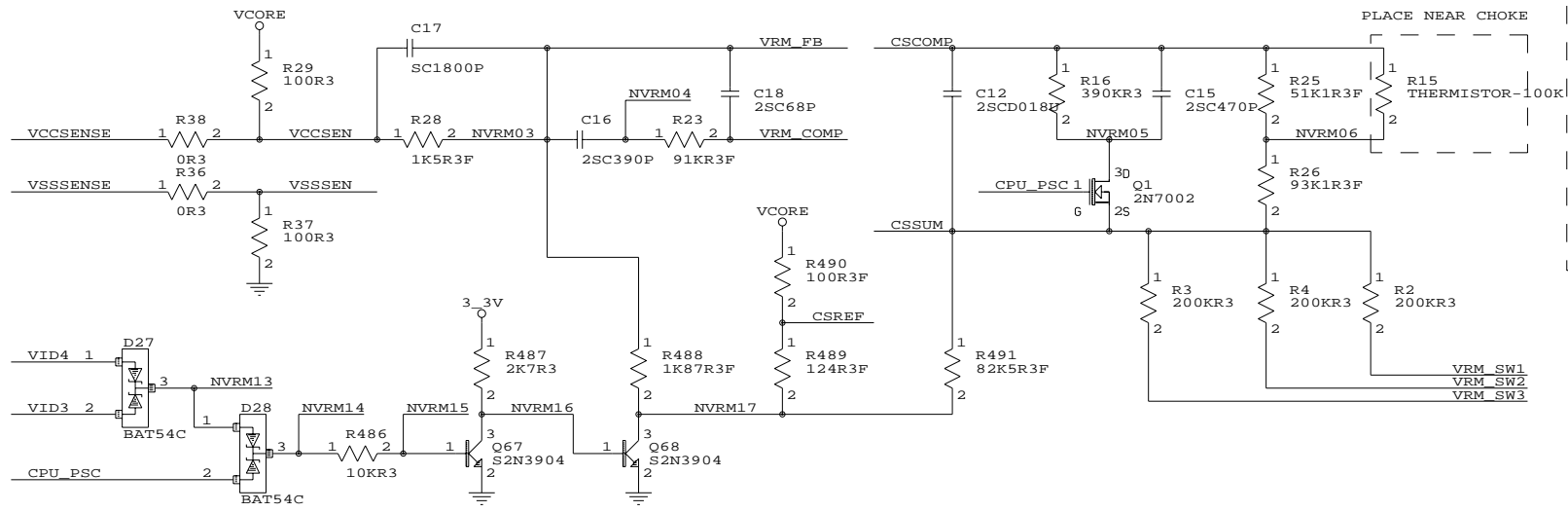
Since it is close to VRM,
remove a 0.1uF cap



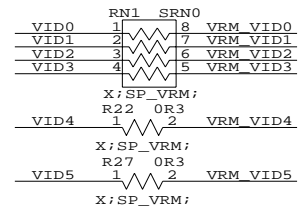
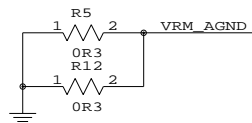
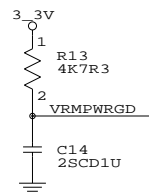
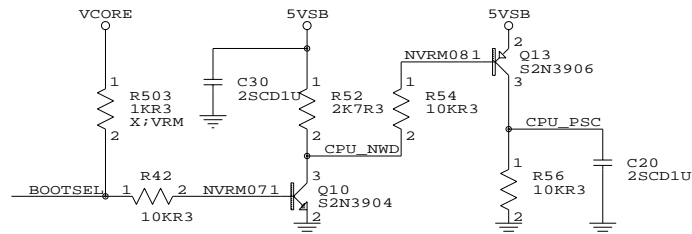
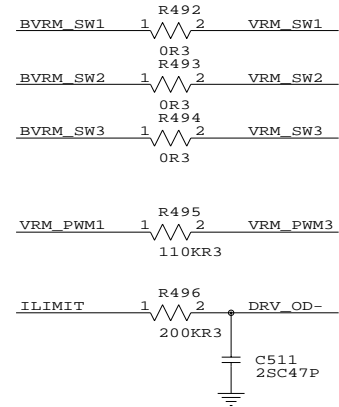
Sinking 50mA during power-on
to reset FAN5093



Albatron Corp.		
Title	ATX_CONN	
Size	Document Number	REV
B	PX865PE PROII	1.0
Date:	March 28, 2003	Sheet 6 of 46

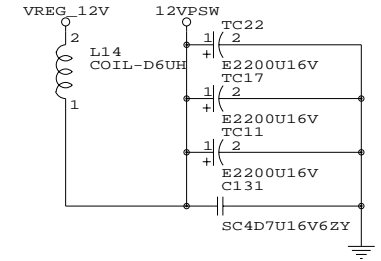
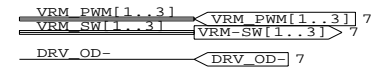
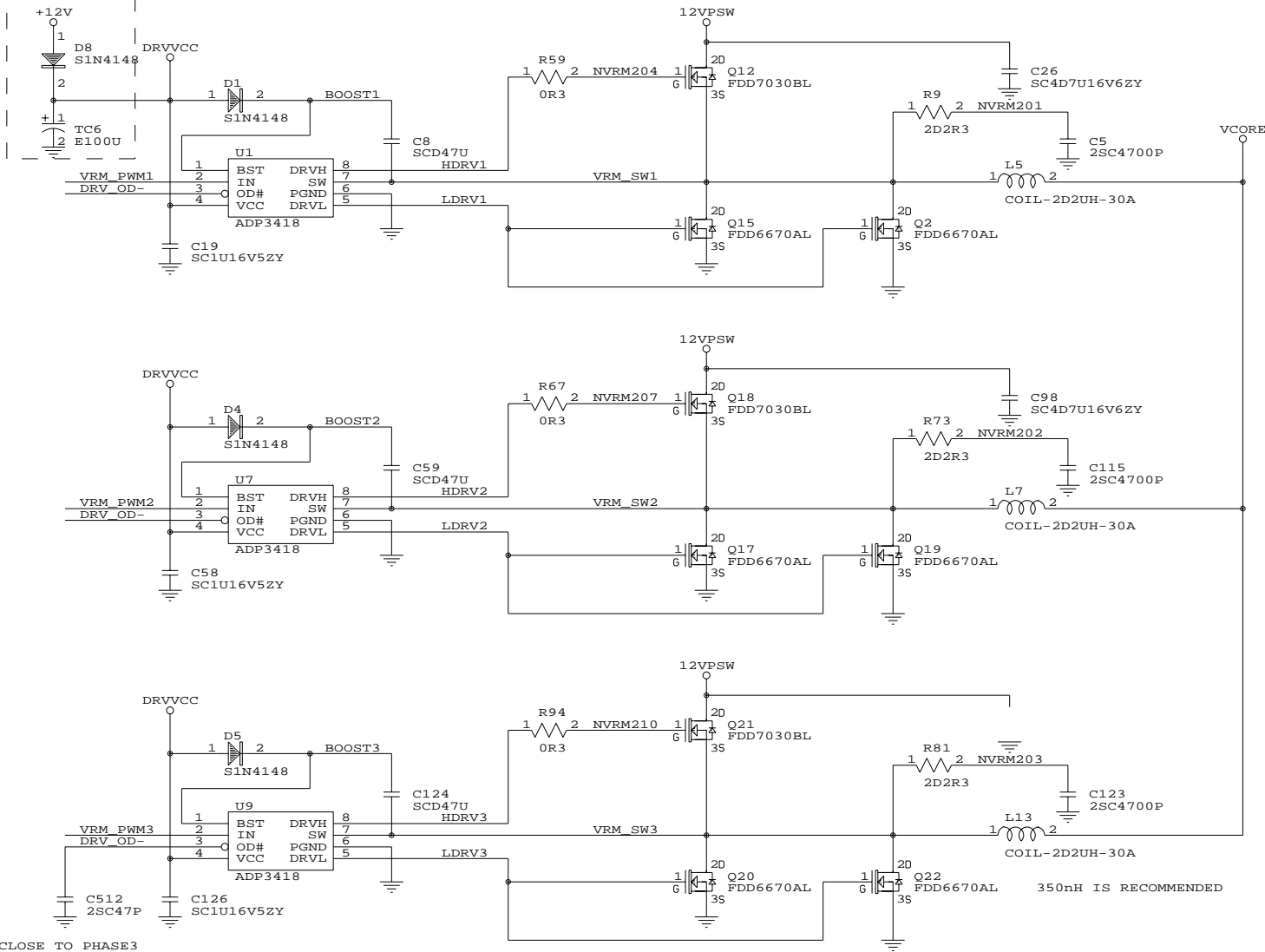


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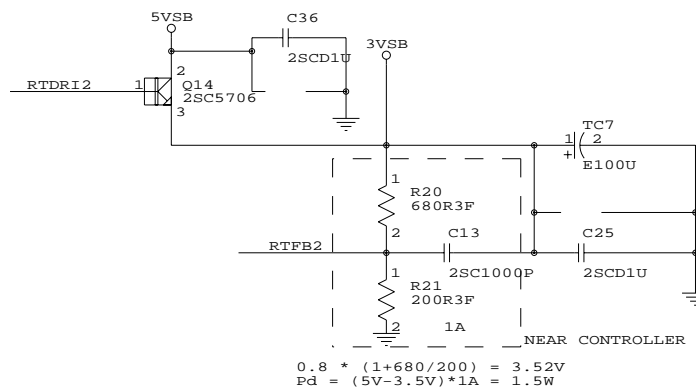
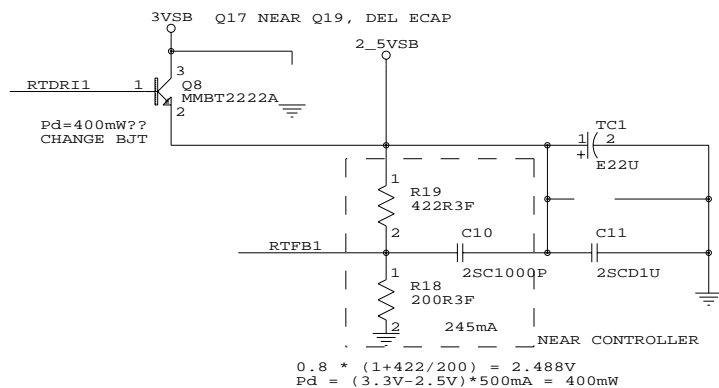
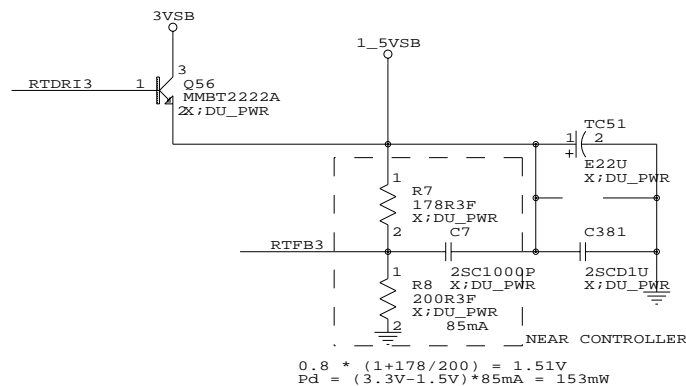
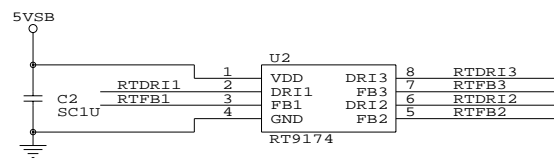
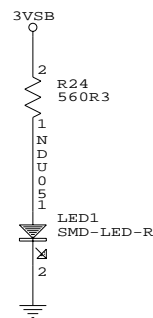
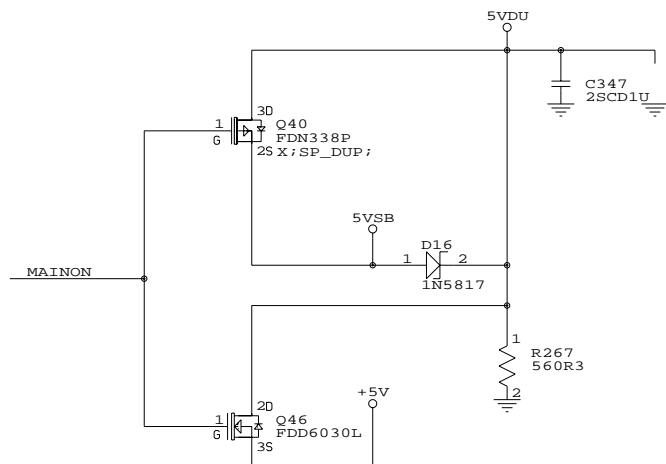


ALBATRON CORP.		
Title	VRD10 - 1 (ADP3168)	
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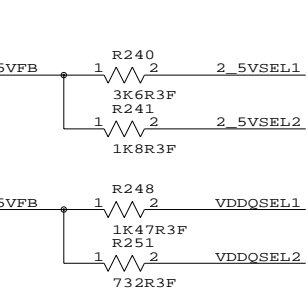
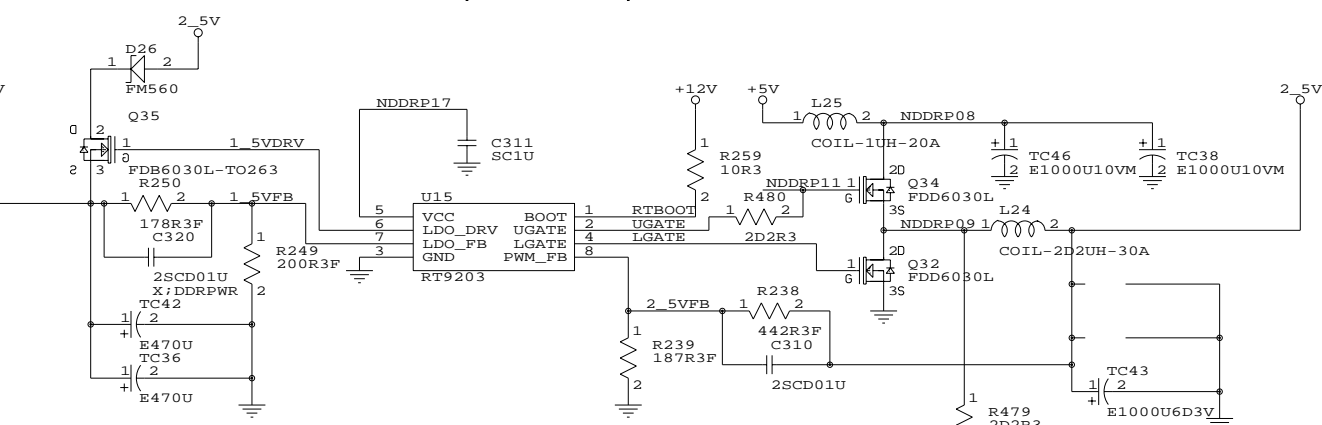
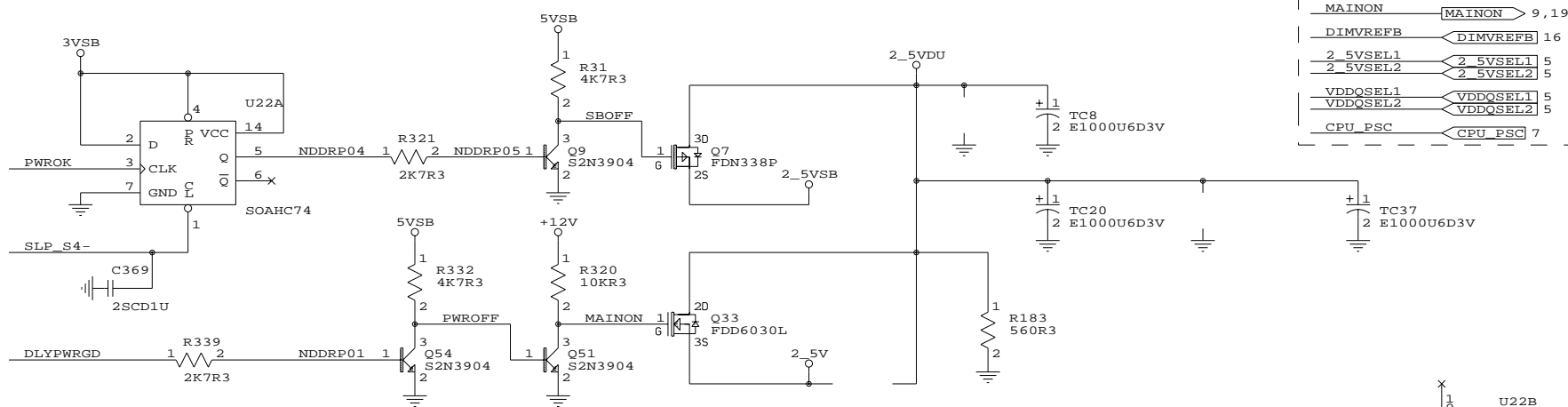
Protection circuits for
+12V short



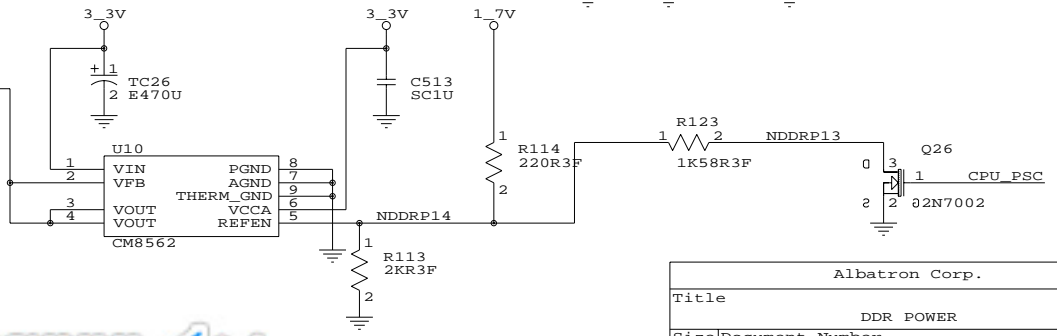
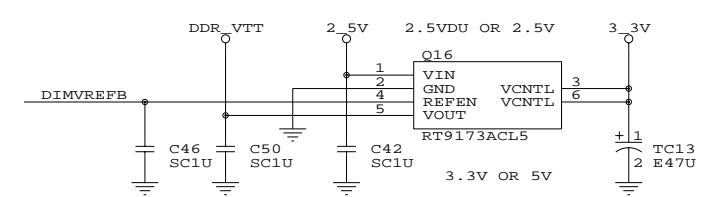
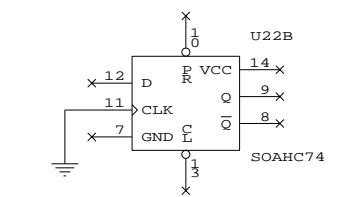
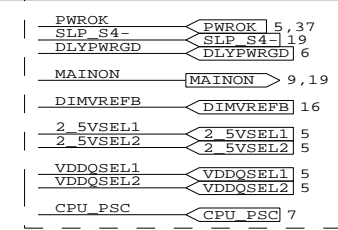
CLOSE TO PHASE3



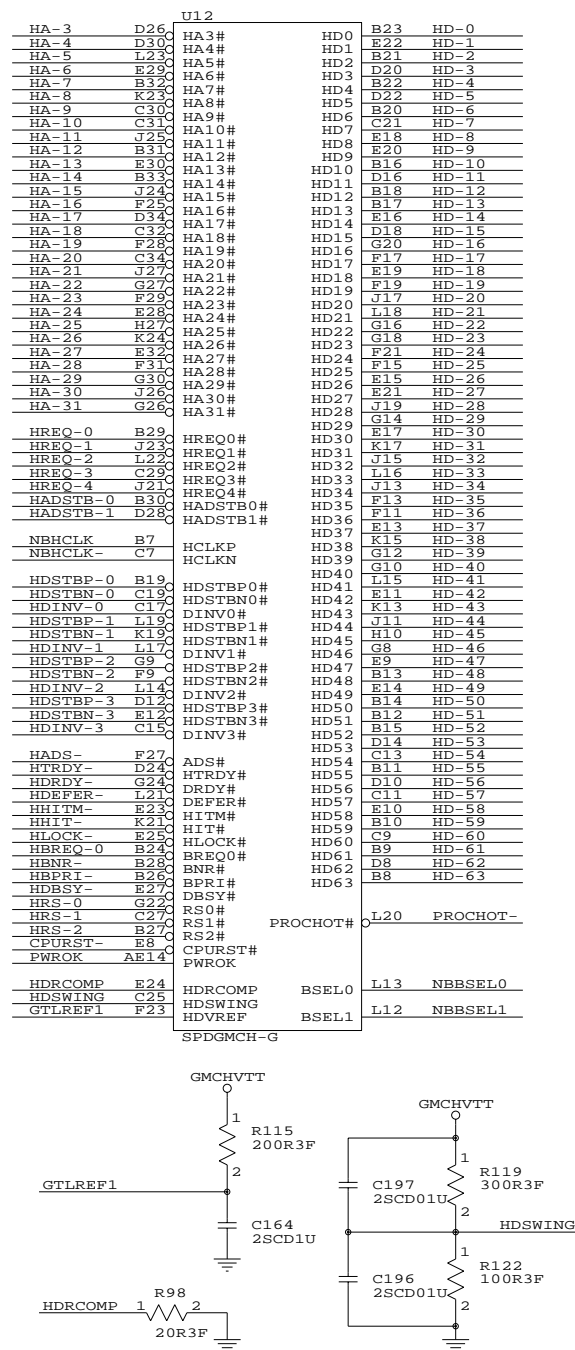
MAINON MAINON 10

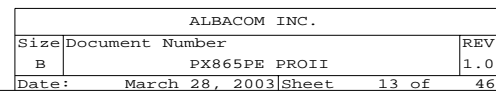


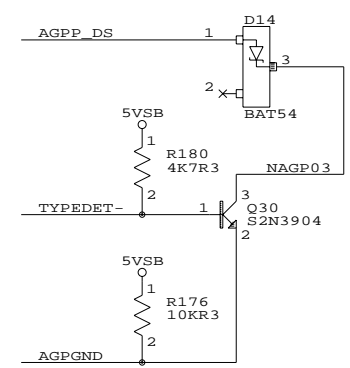
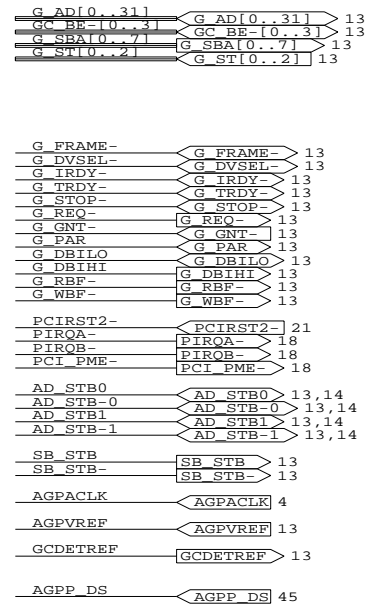
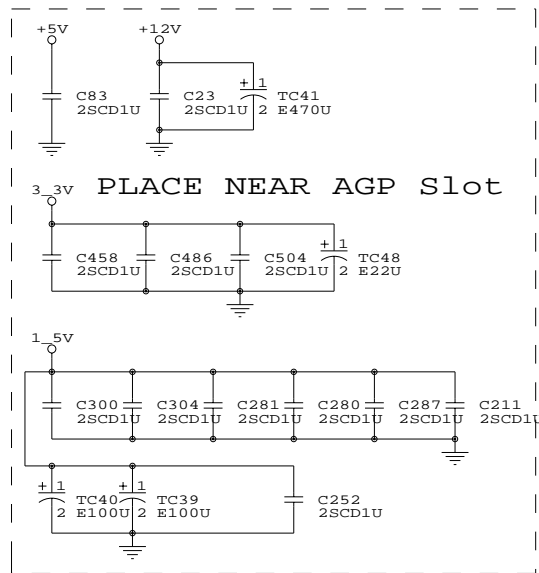
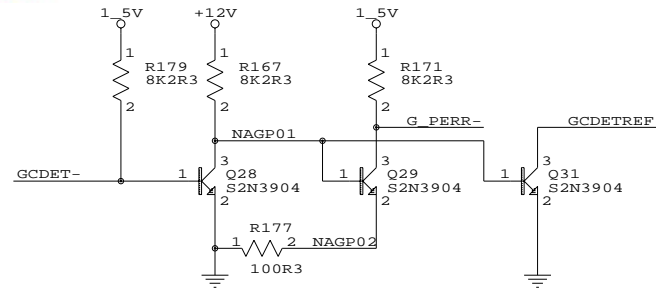
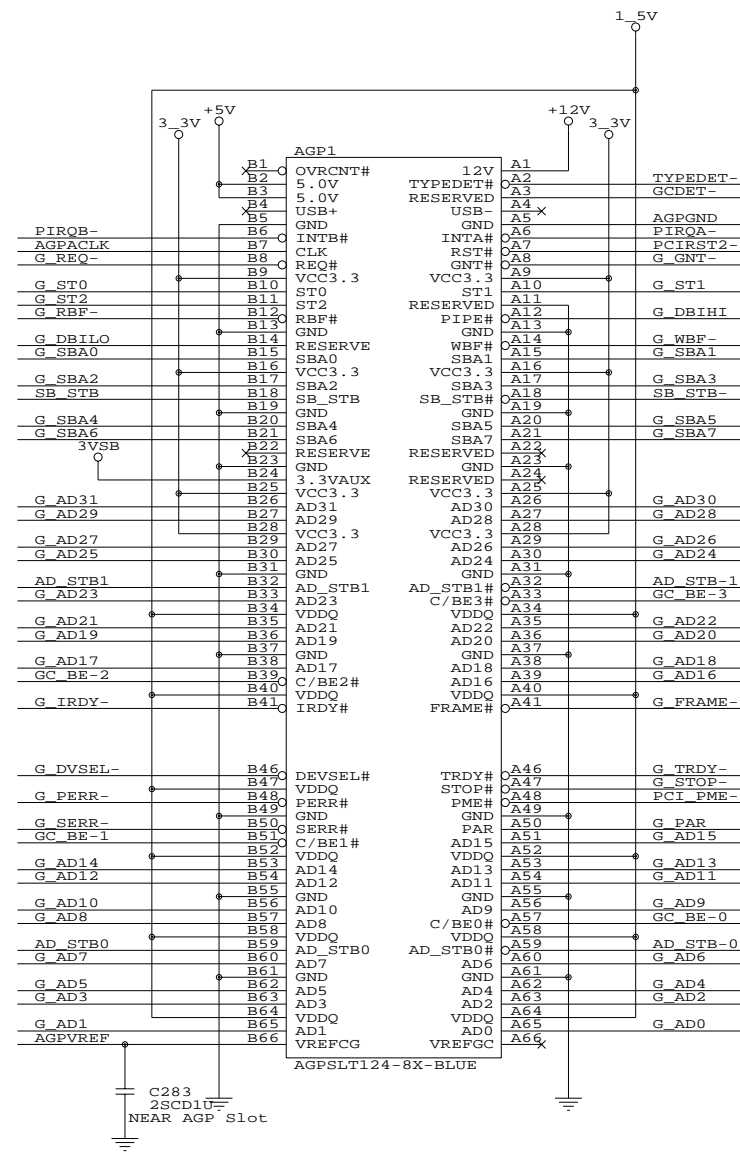
$787/124=107.12$
 $1.25 * (1+124/107.12) = 2.70V$
 $1240/124=112.73$
 $1.25 * (1+124/112.73) = 2.63V$
 $1240/787/124=98.6$
 $1.25 * (1+124/98.6) = 2.82V$



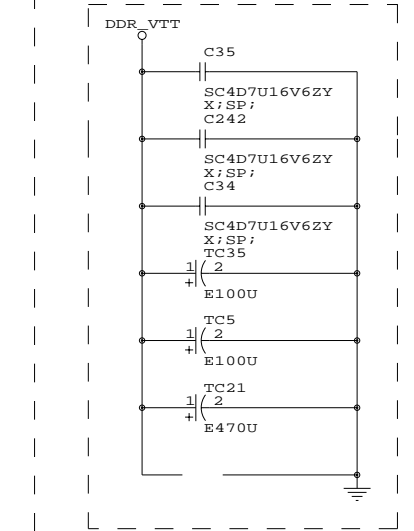
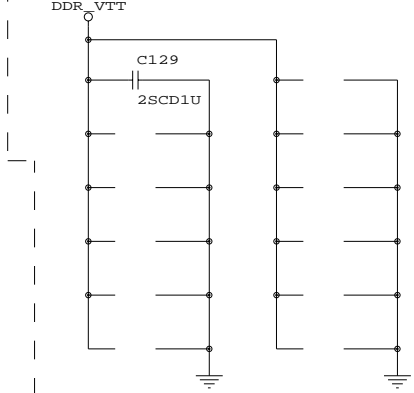
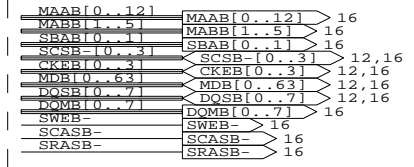
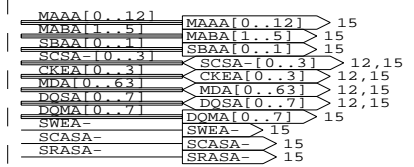
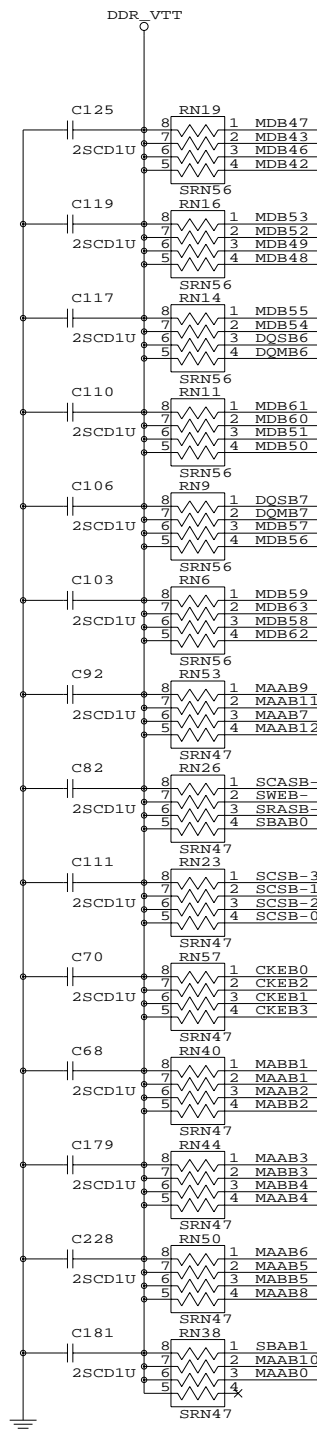
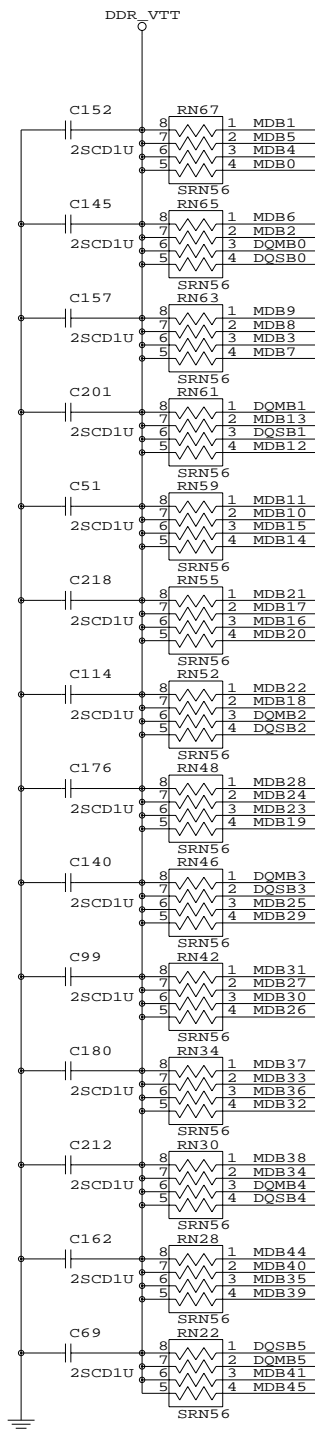
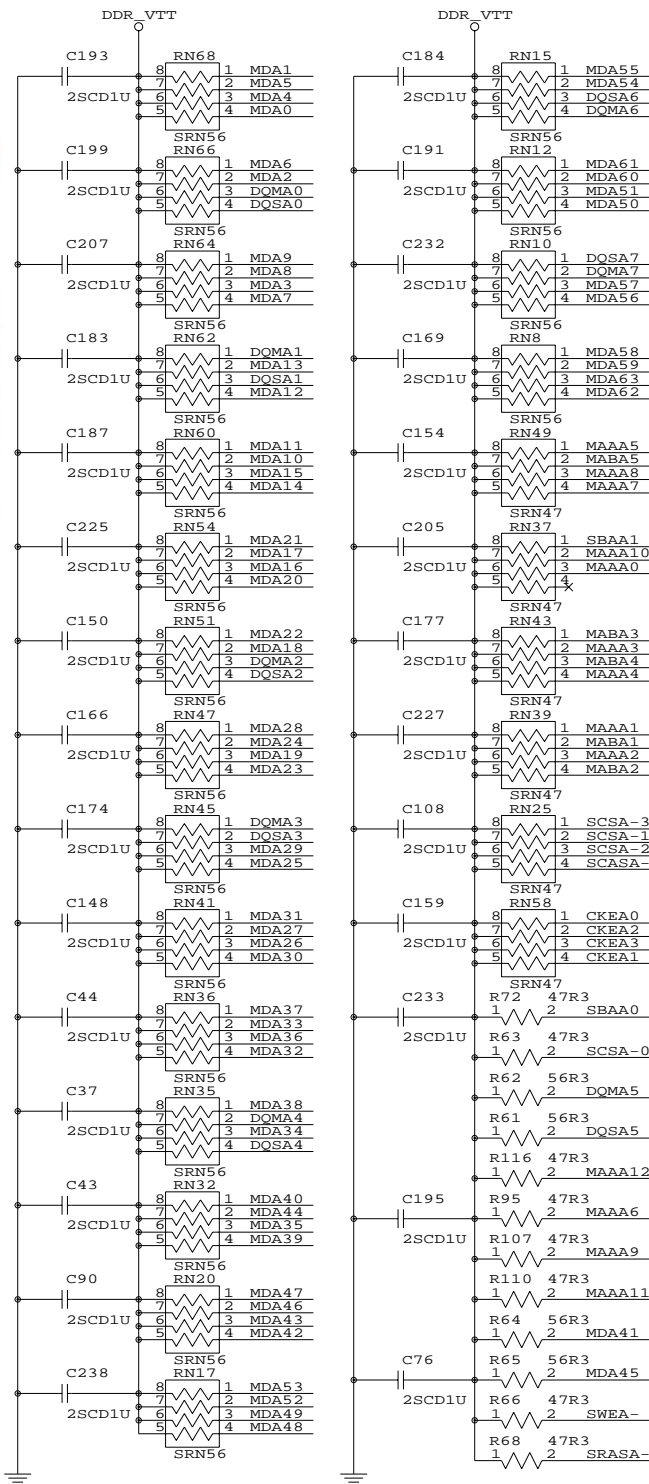
Albatron Corp.		
Title		
DDR POWER		
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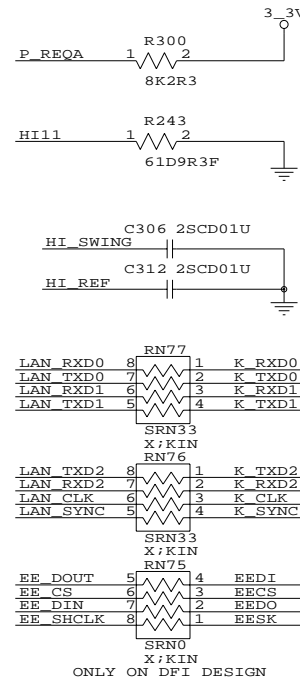
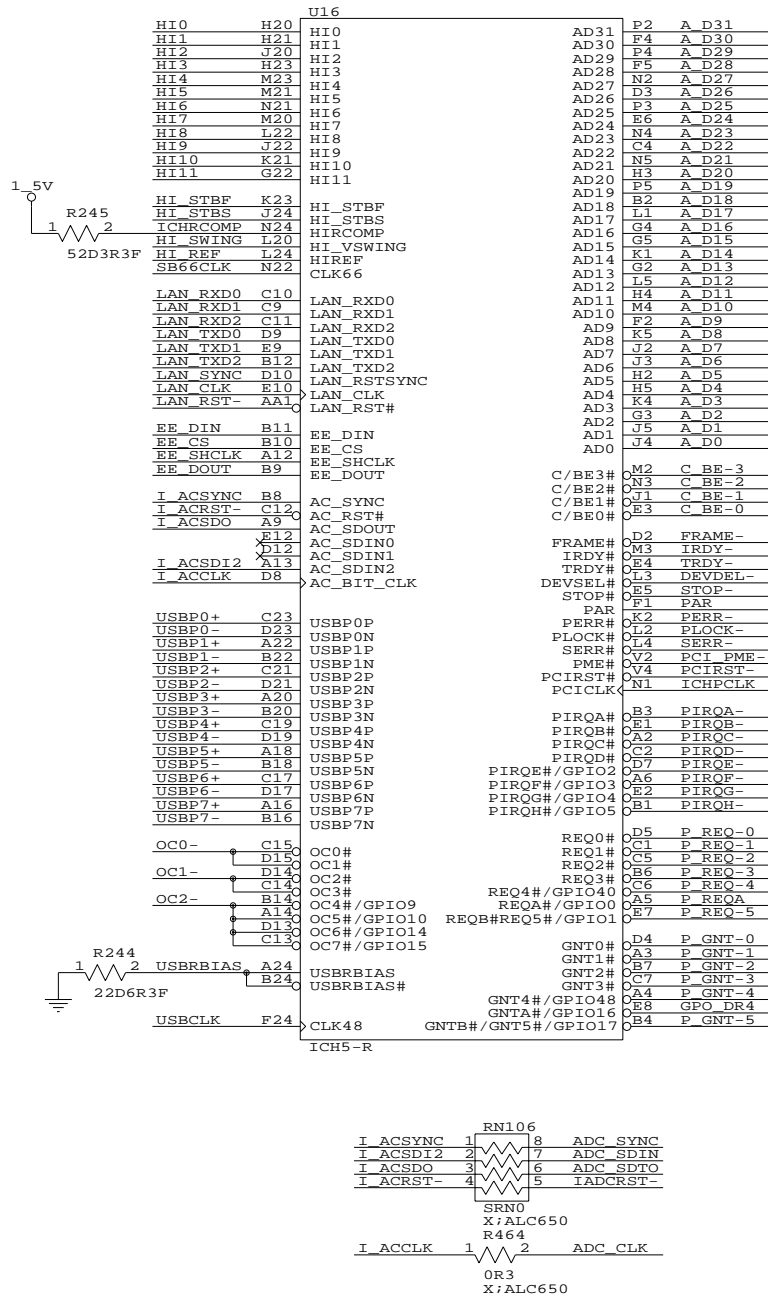




Albatron Corp.		
Title		
AGP SLOT		
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Albatron Corp.		
Title		
DDR_TERMINATION		
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A D[0..31] A D[0..31] 23,24,25,26,27,28,29,30 C BE-[0..3] C BE-[0..3] 23,24,25,26,27,28,29,30

PIROA- PIRQA- 14,23,31 PIROB- PIRQB- 14,23,30,31 PIROC- PIRQC- 23,24,25,26,27,31 PIROD- PIRQD- 23,24,25,26,27,31 PIROE- PIRQE- 29,31 PIROF- PIRQF- 24,25,26,27,31 PIROG- PIRQG- 28,31 PIROH- PIRQH- 24,25,26,27,31

P REQ-5 P REQ-5 27,29,31 P REQ-4 P REQ-4 26,28,31 P REQ-3 P REQ-3 25,30,31 P REQ-2 P REQ-2 25,26,27,31 P REQ-1 P REQ-1 31 P REQ-0 P REQ-0 23,31

P GNT-5 P GNT-5 27,29 P GNT-4 P GNT-4 26,28 P GNT-3 P GNT-3 25,30 P GNT-2 P GNT-2 25,26,27,31 P GNT-1 P GNT-1 31 P GNT-0 P GNT-0 23

FRAME- FRAME- 23,24,25,26,27,28,29,30,31 IRDY- IRDY- 23,24,25,26,27,28,29,30,31 TRDY- TRDY- 23,24,25,26,27,28,29,30,31 DEVDEL- DEVSEL- 23,24,25,26,27,28,29,30,31 STOP- STOP- 23,24,25,26,27,28,29,30,31 PAR PAR > 23,24,25,26,27,28,29,30 PERR- PERR- 23,24,25,26,27,28,31 PLOCK- PLOCK- 23,24,25,26,27,31 SERR- SERR- 23,24,25,26,27,31 PCI PME- PCI PME- 14,23,24,25,26,27,28,31 PCIRST- PCIRST- 21 ICHPCLK ICHPCLK 4

HI[0..10] HI[0..10] 13

HI STBS HI STBS 13 HI STBF HI STBF 13 HI SWING HI SWING 13 HI REF HI REF 13 SB66CLK SB66CLK 4

ADC_SDIN ADC_SDIN 43 ADC_SYNC ADC_SYNC 43 ADC_SDTO ADC_SDTO 43 ADC_CLK ADC_CLK 43 IADCRST- IADCRST- 30

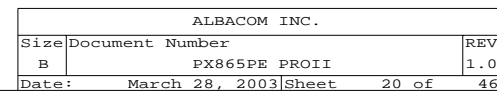
USBP0+ USBP0+ 35 USBP0- USBP0- 35 USBP1+ USBP1+ 35 USBP1- USBP1- 35 USBP2+ USBP2+ 36 USBP2- USBP2- 36 USBP3+ USBP3+ 36 USBP3- USBP3- 36 USBP4+ USBP4+ 36 USBP4- USBP4- 36 USBP5+ USBP5+ 36 USBP5- USBP5- 36 USBP6+ USBP6+ 36 USBP6- USBP6- 36 USBP7+ USBP7+ 36 USBP7- USBP7- 36

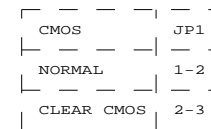
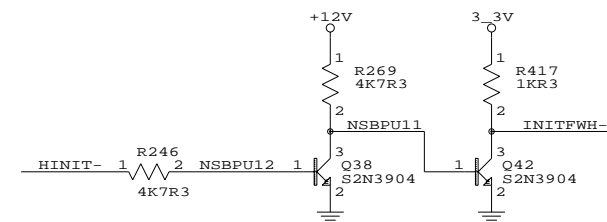
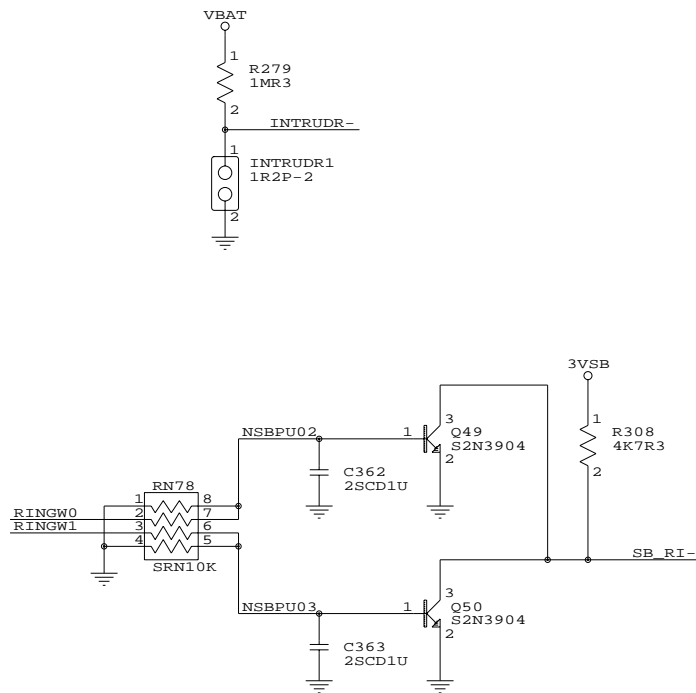
OC0- OC0- 35 OC1- OC1- 36 OC2- OC2- 36

USBCLK USBCLK 4

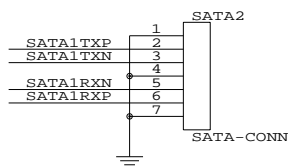
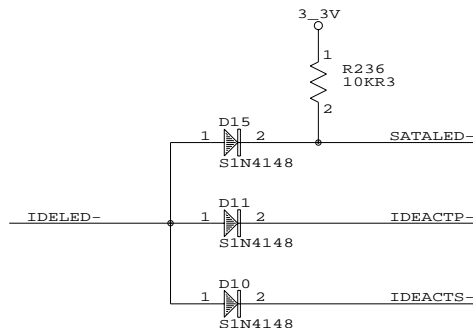
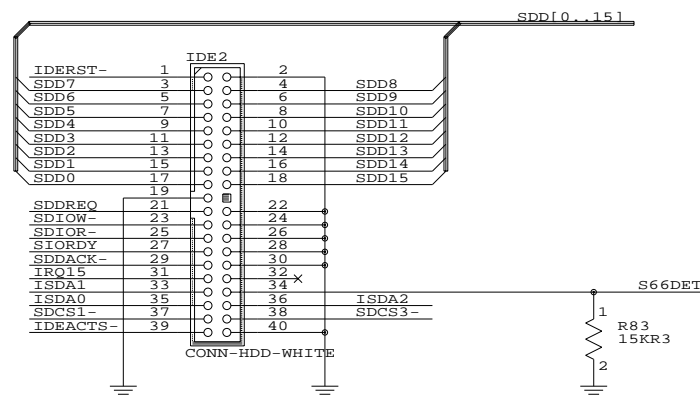
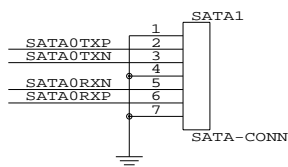
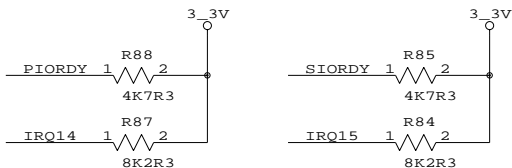
K_RXD0 K_RXD0 32 K_RXD1 K_RXD1 32 K_RXD2 K_RXD2 32 K_TXD0 K_TXD0 32 K_TXD1 K_TXD1 32 K_TXD2 K_TXD2 32 K_SYNC K_SYNC 32 EEDI EEDI 32 EECS EECS 32 EESK EESK 32 EEDO EEDO 32 LAN_RST- LAN_RST- 32

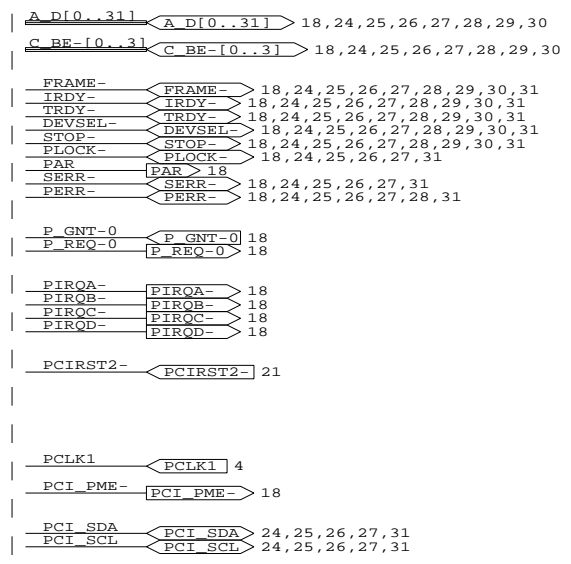
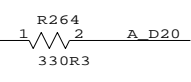
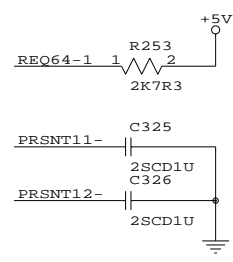
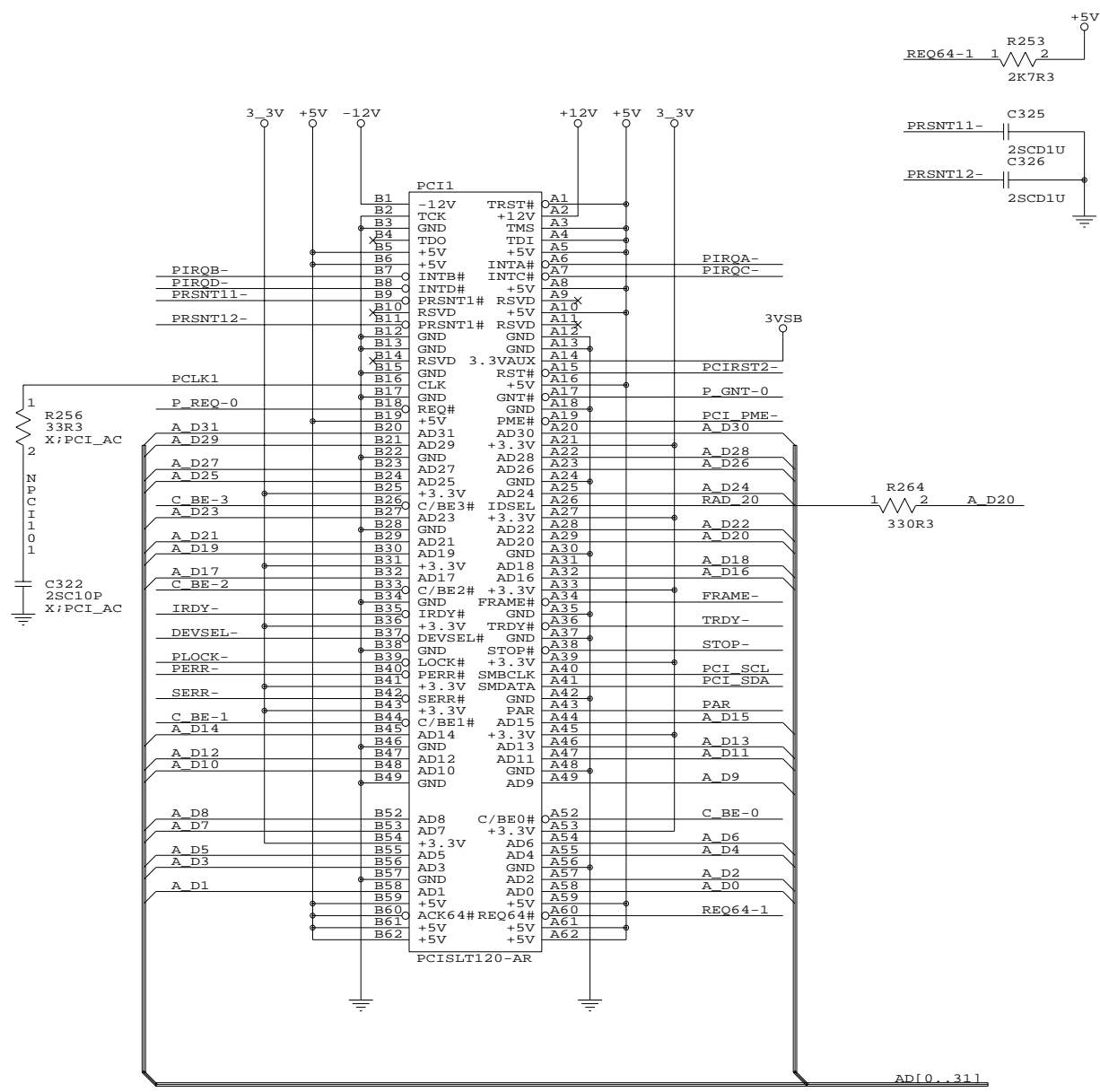
GPO_DR4 GPO_DR4 45

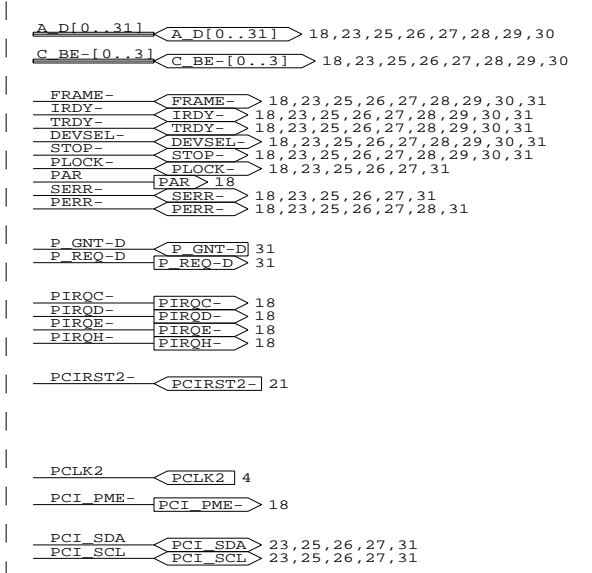
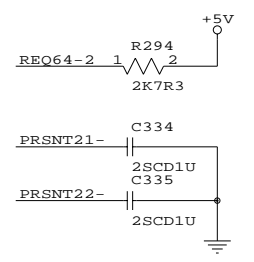
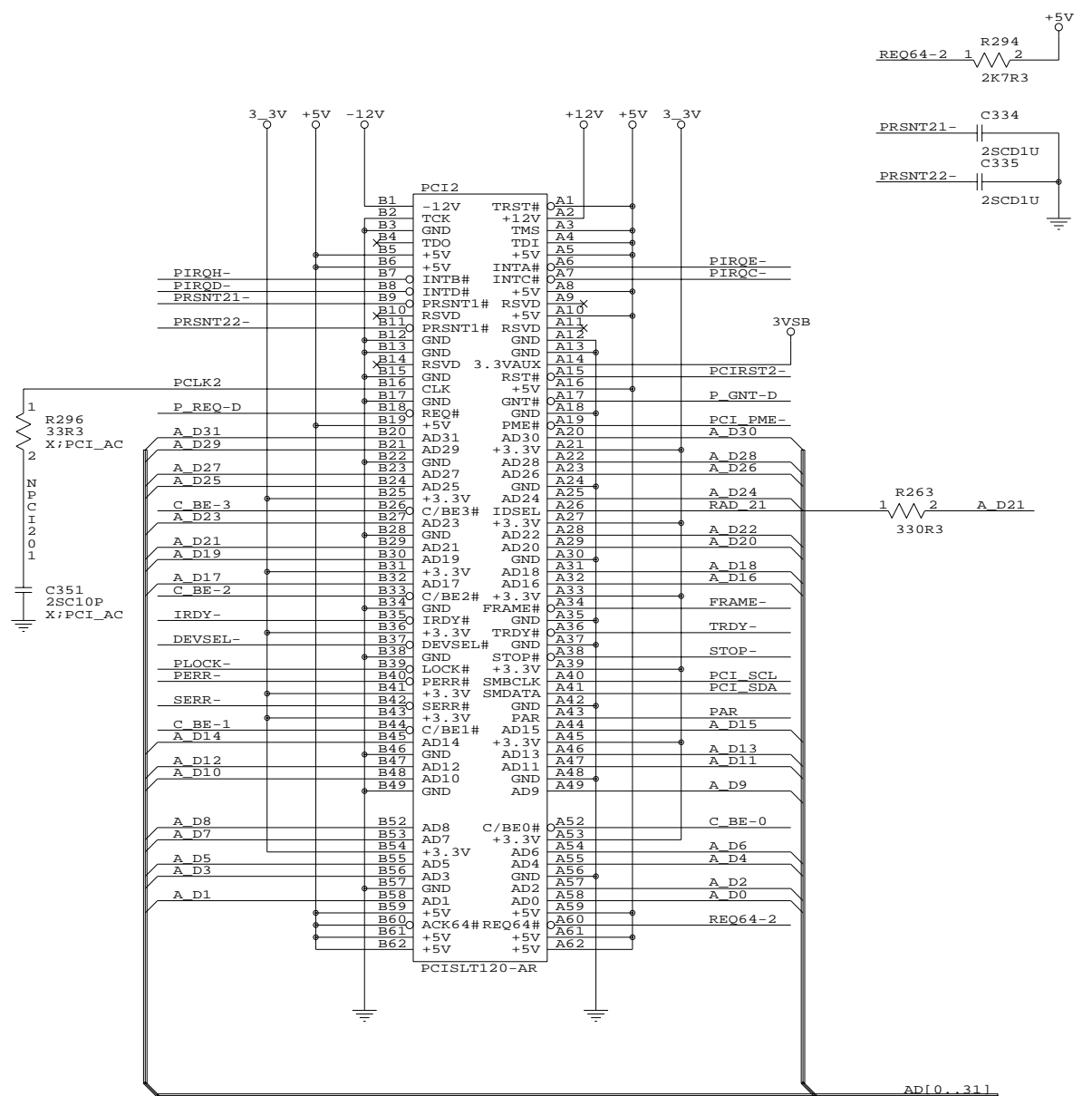


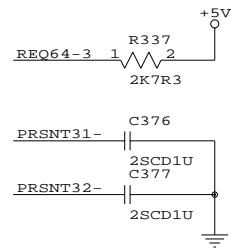
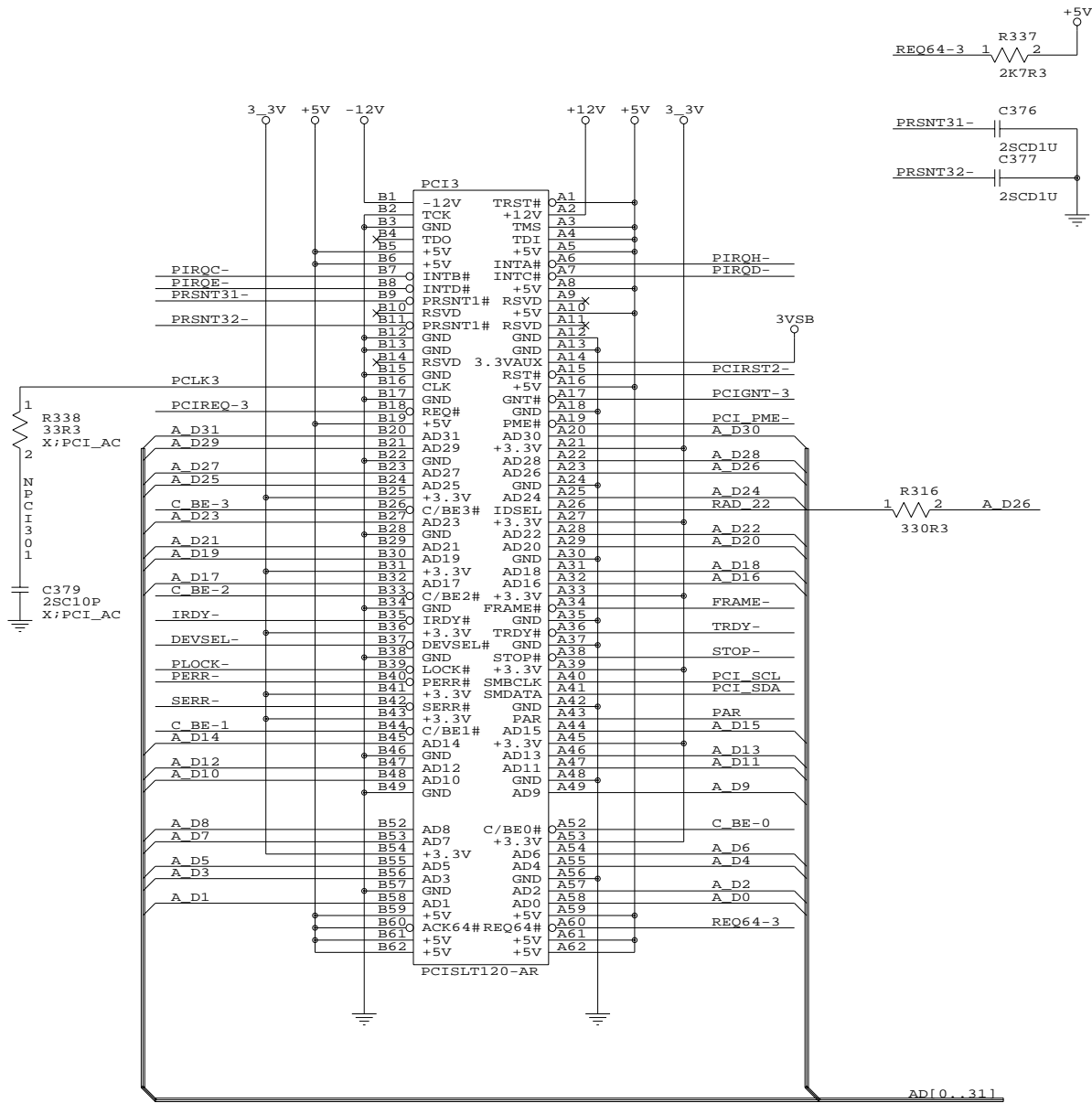


Albatron Corp.			
Title			
ICH4 PULL/DOWN			
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A D[0..31] A D[0..31] 18, 23, 24, 26, 27, 28, 29, 30
C BE-[0..3] C BE-[0..3] 18, 23, 24, 26, 27, 28, 29, 30

FRAME- FRAME- 18, 23, 24, 26, 27, 28, 29, 30, 31
IRDY- IRDY- 18, 23, 24, 26, 27, 28, 29, 30, 31
TRDY- TRDY- 18, 23, 24, 26, 27, 28, 29, 30, 31
DEVSEL- DEVSEL- 18, 23, 24, 26, 27, 28, 29, 30, 31
STOP- STOP- 18, 23, 24, 26, 27, 28, 29, 30, 31
PLOCK- PLOCK- 18, 23, 24, 26, 27, 31
PAR PAR 18
PERR- SERR- 18, 23, 24, 26, 27, 31
PERR- PERR- 18, 23, 24, 26, 27, 28, 31

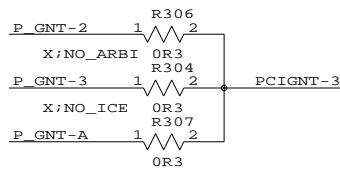
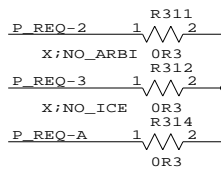
P GNT-2 P GNT-2 18
P GNT-3 P GNT-3 18
P GNT-A P GNT-A 31
P REQ-2 P REQ-2 18
P REQ-3 P REQ-3 18
P REQ-A P REQ-A 31

PIROC- PIOC- 18
PIROD- PIROD- 18
PIROE- PIROE- 18
PIROH- PIROH- 18

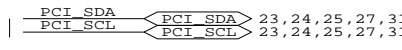
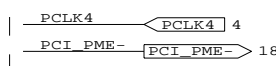
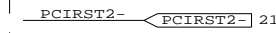
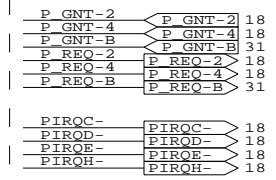
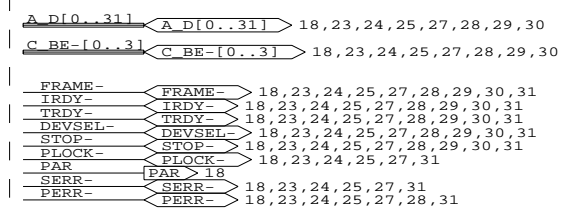
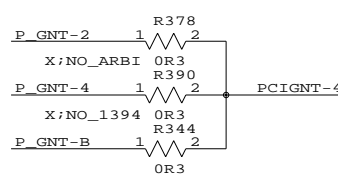
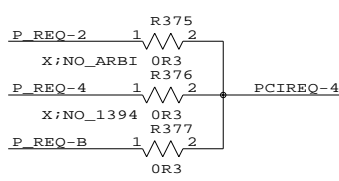
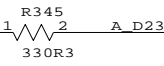
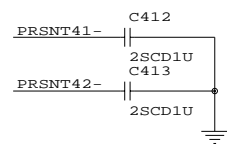
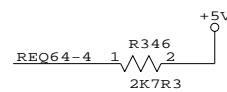
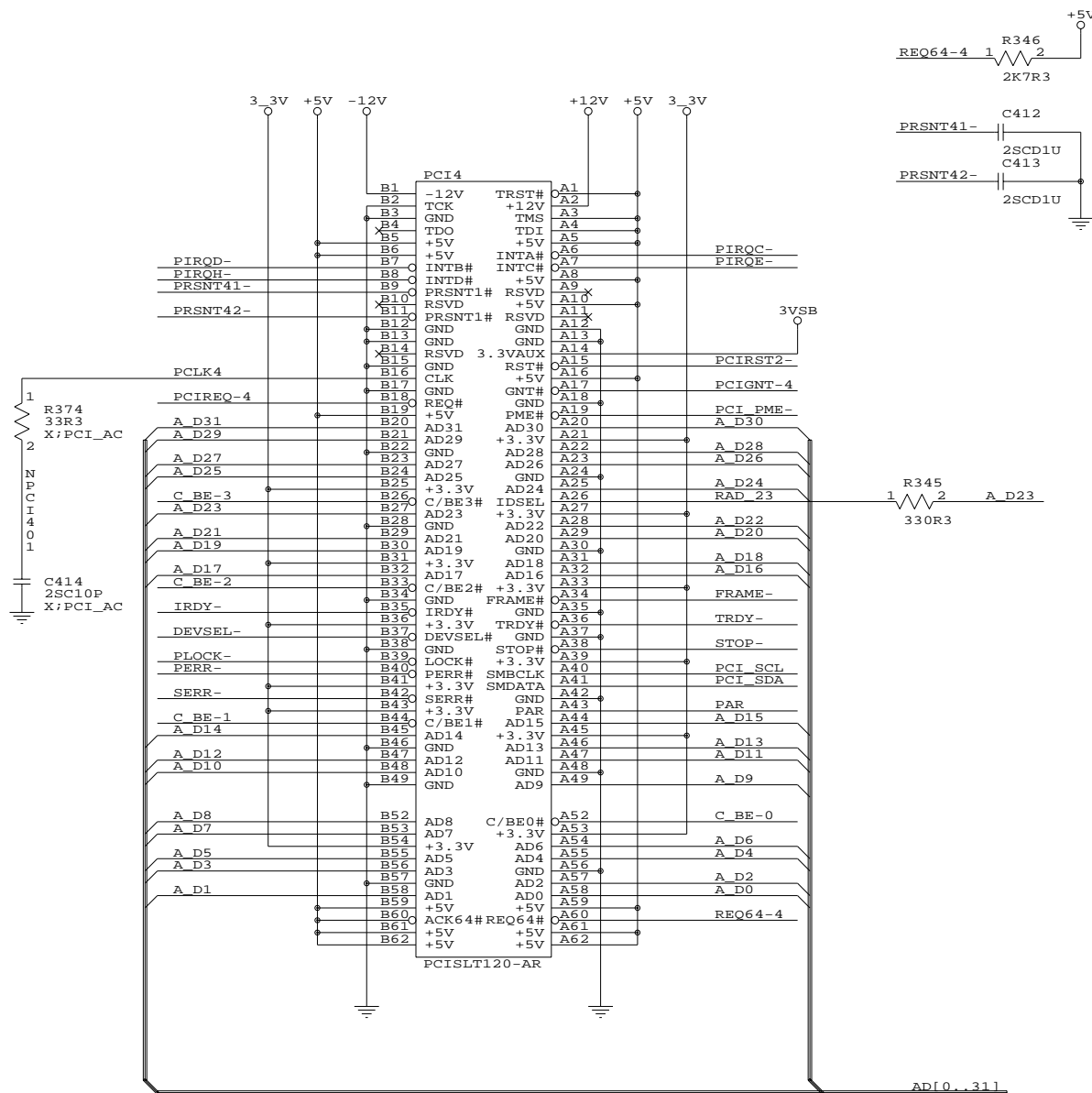
PCIRST2- PCIRST2- 21

PCLK3 PCLK3 4
PCI PME- PCI PME- 18

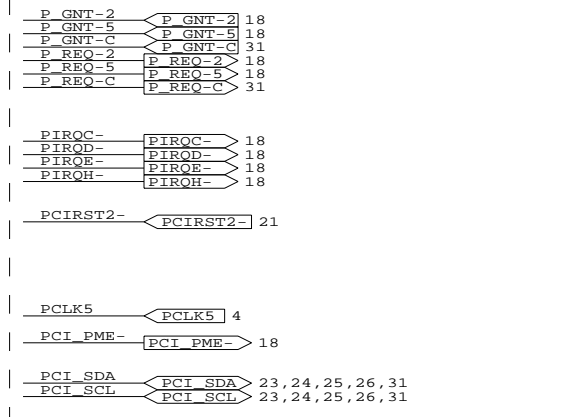
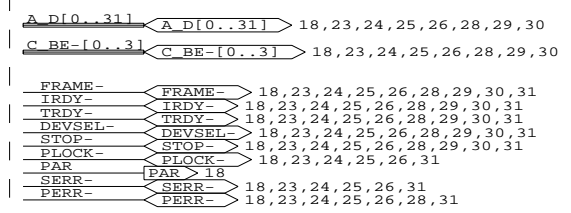
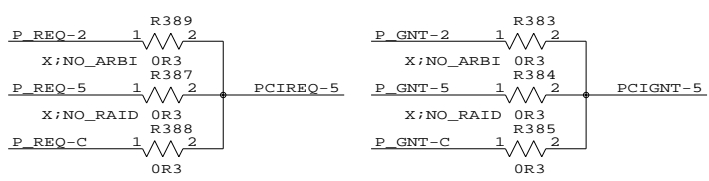
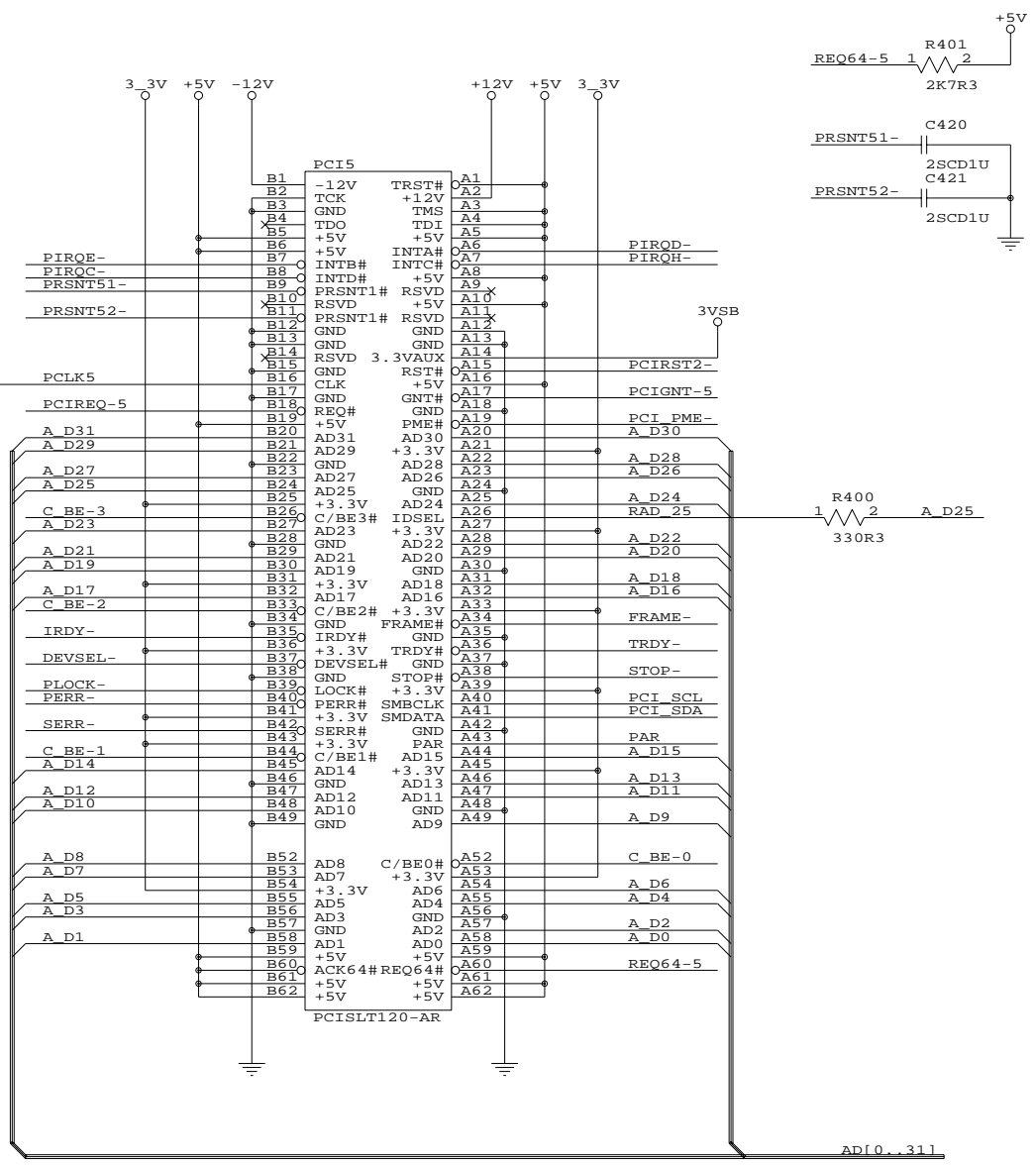
PCI SDA PCI SDA 23, 24, 26, 27, 31
PCI SCL PCI SCL 23, 24, 26, 27, 31



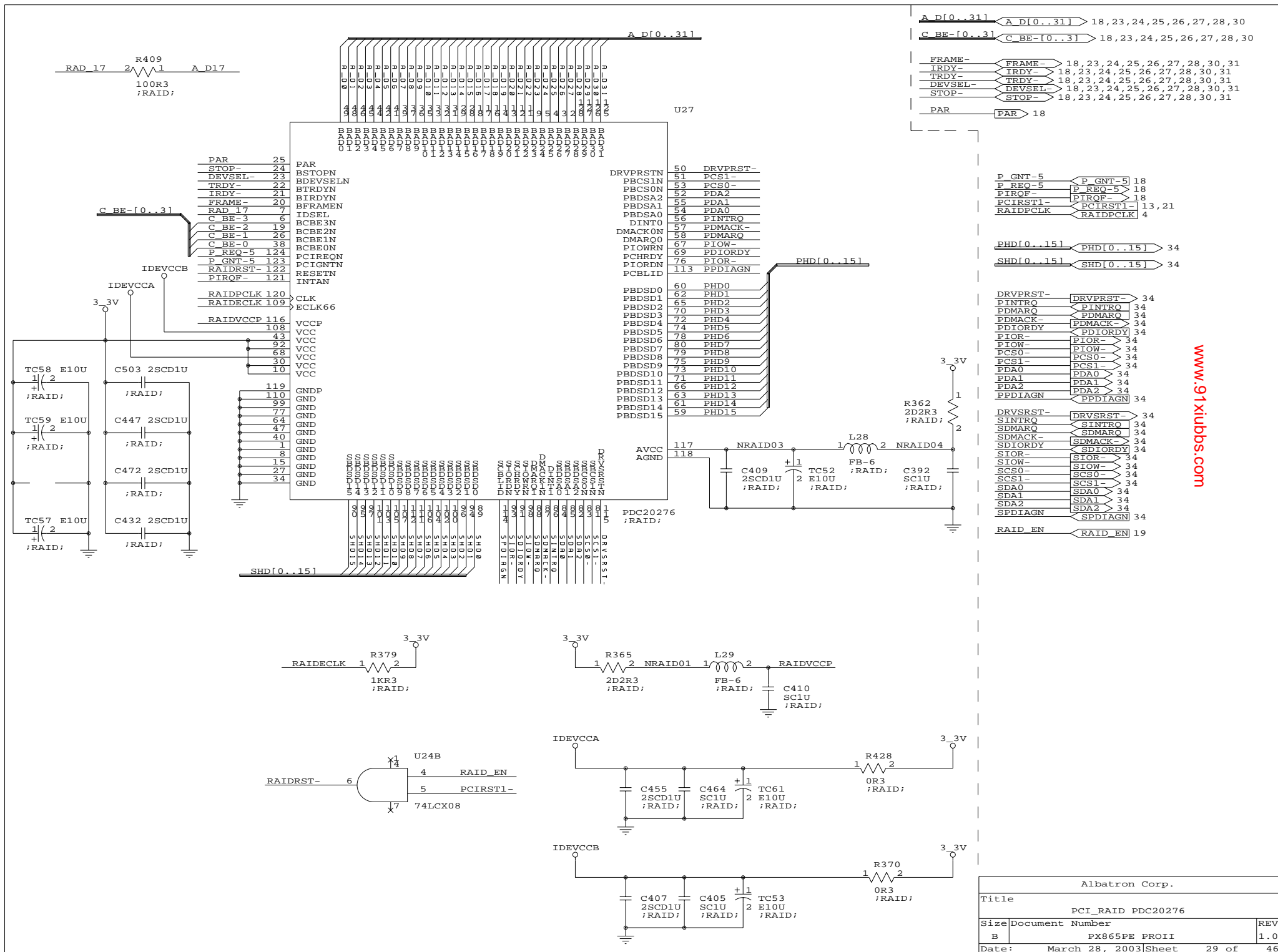
Albatron Corp.		
Title		
PCI SLOT3		
Size	Document Number	REV
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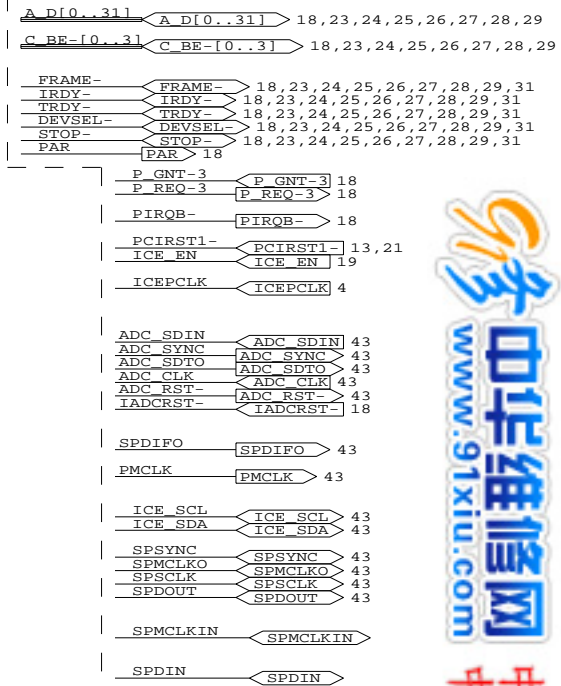
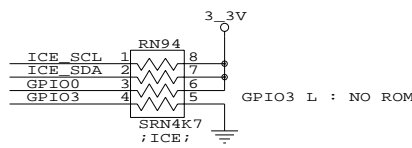
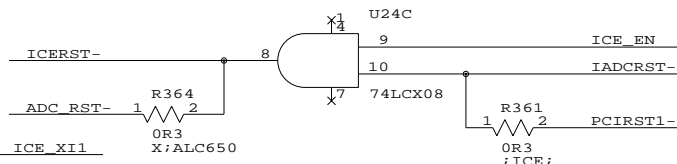
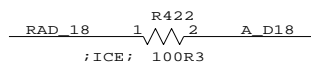
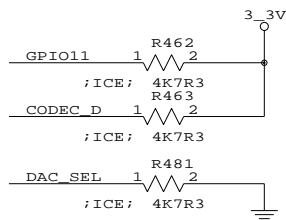
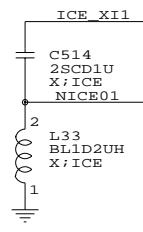
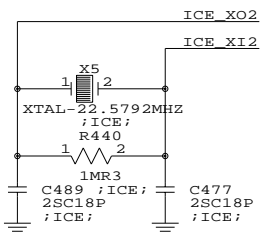
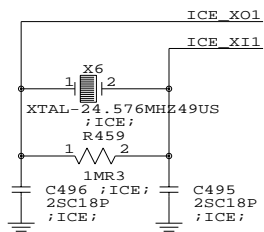
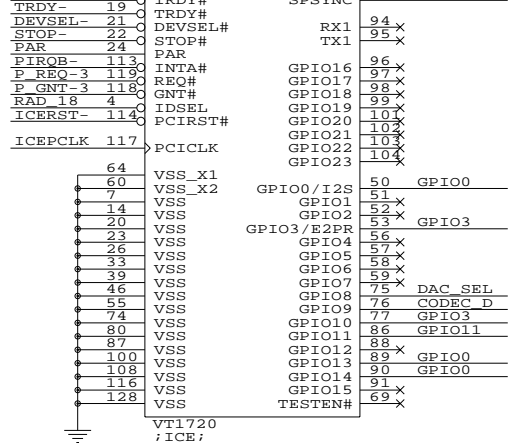
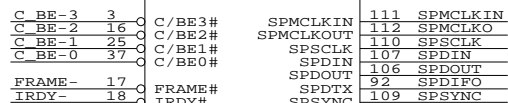
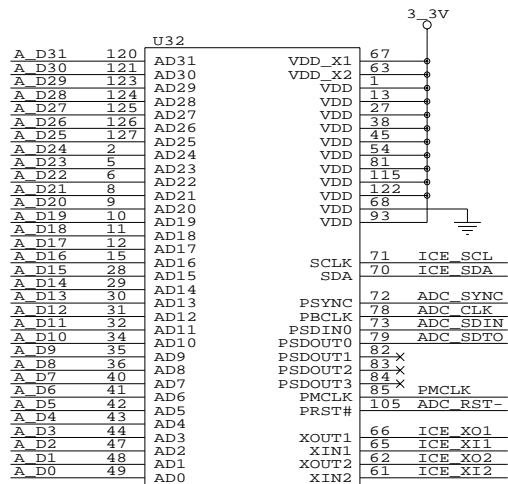


Albatron Corp.		
Title		
PCI SLOT 4		
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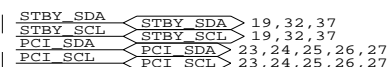
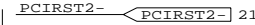
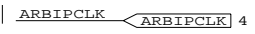
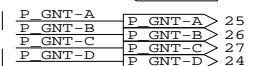
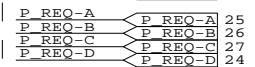
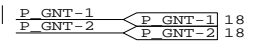
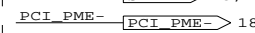
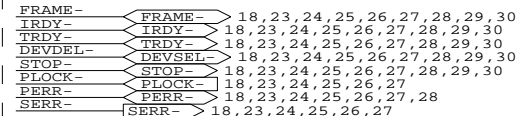
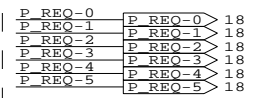
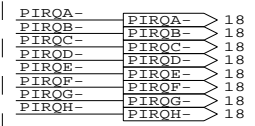
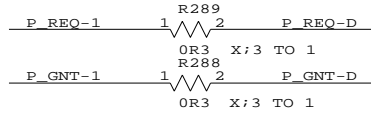
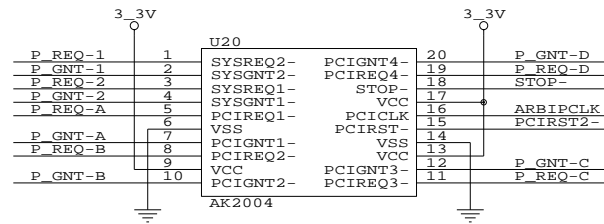
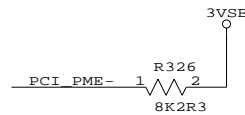
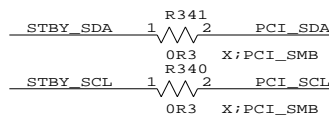
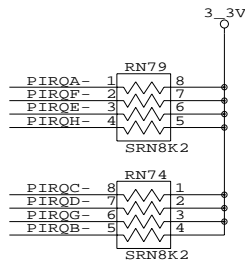
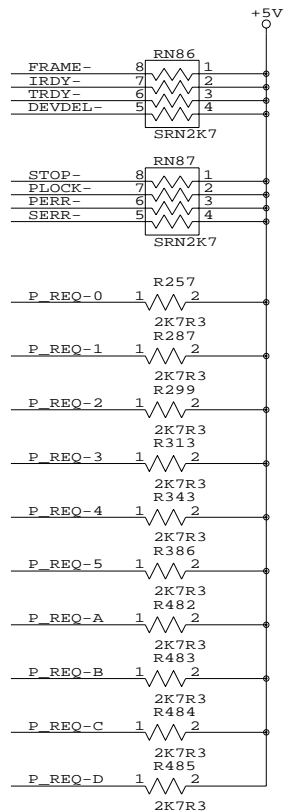


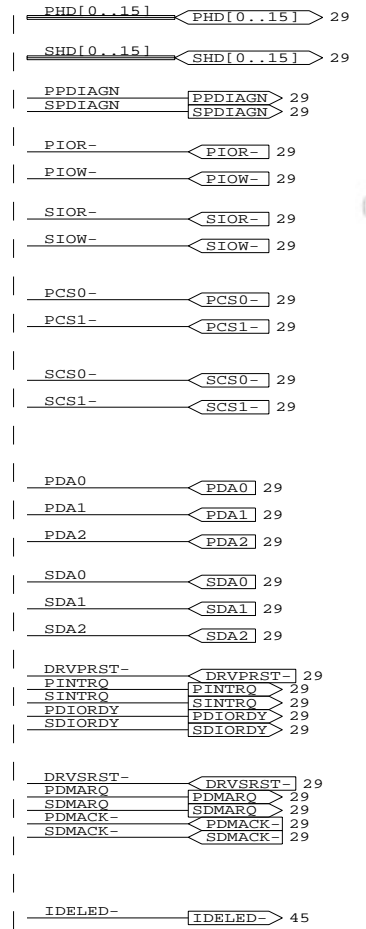
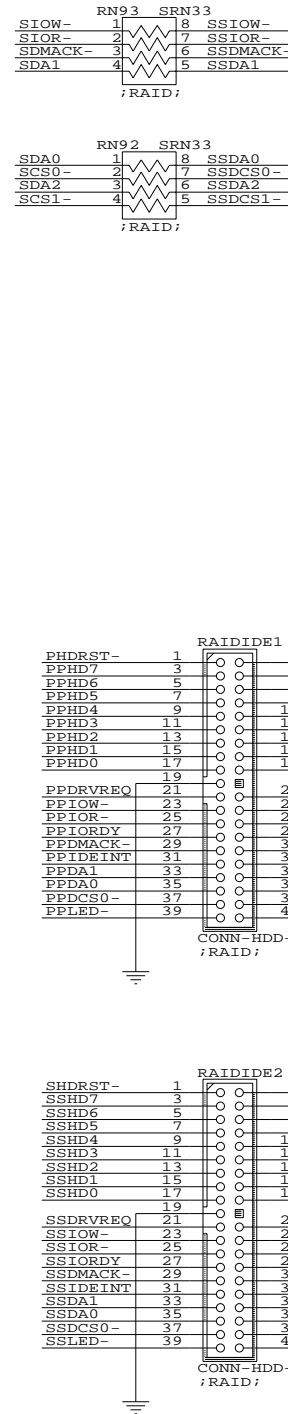
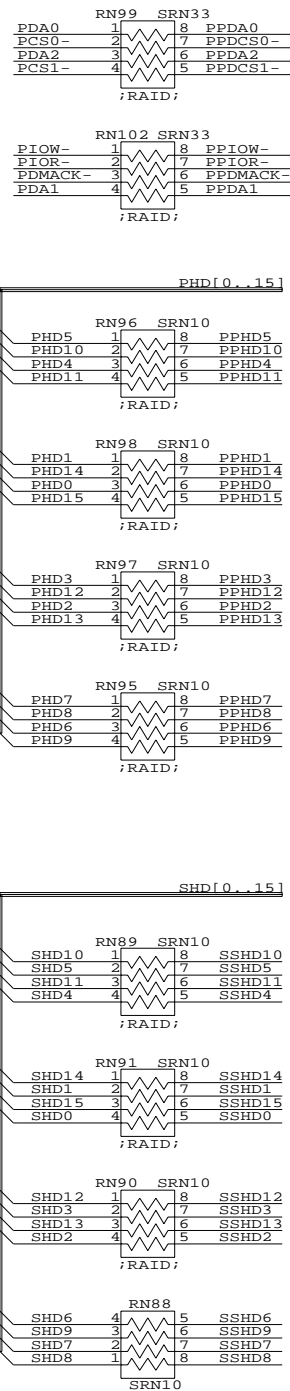
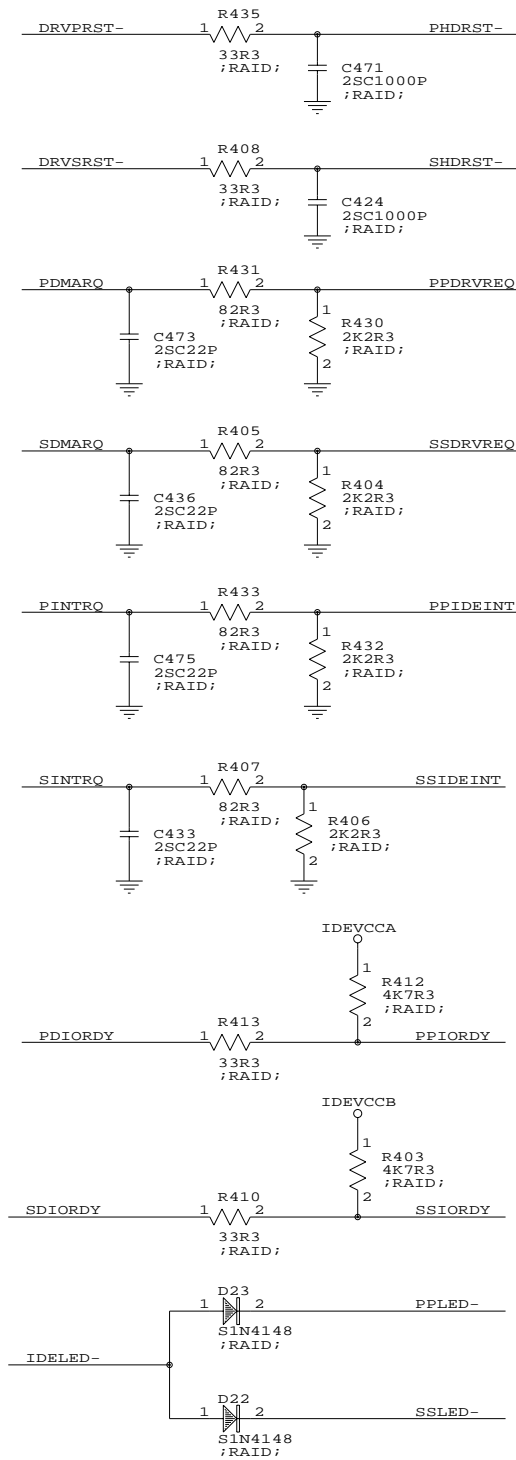
Albatron Corp.		
Title		
PCI SLOT 5		
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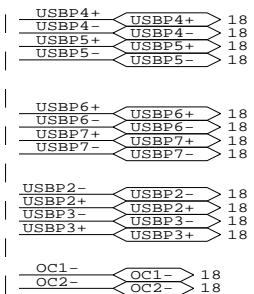
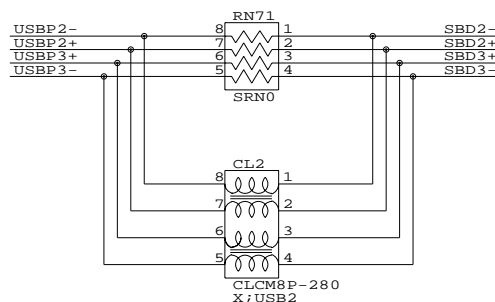
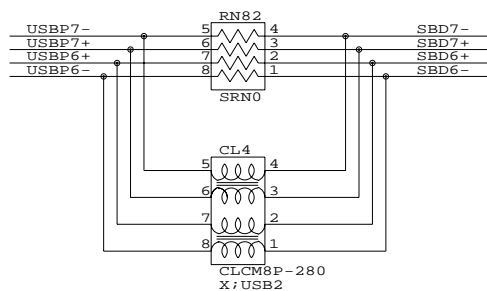
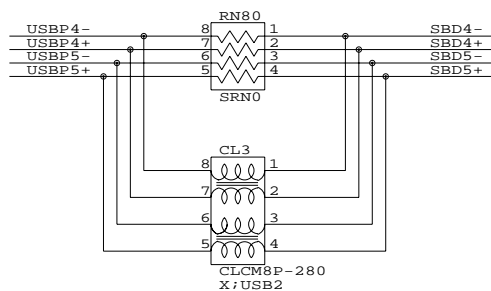
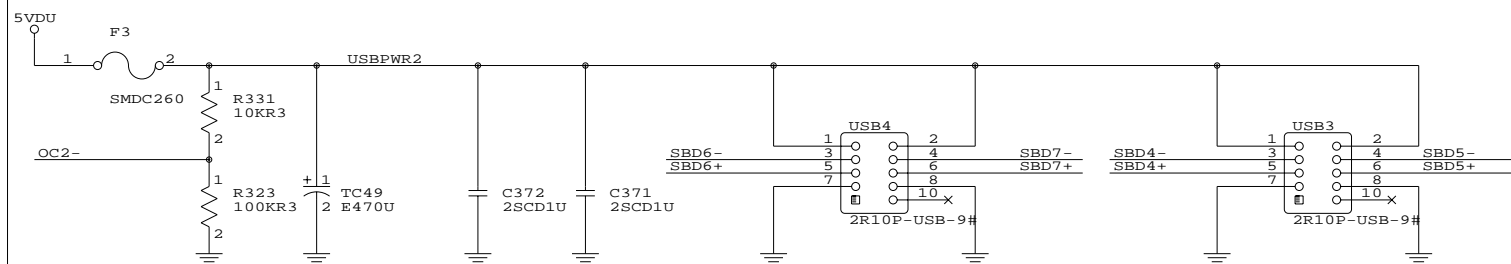
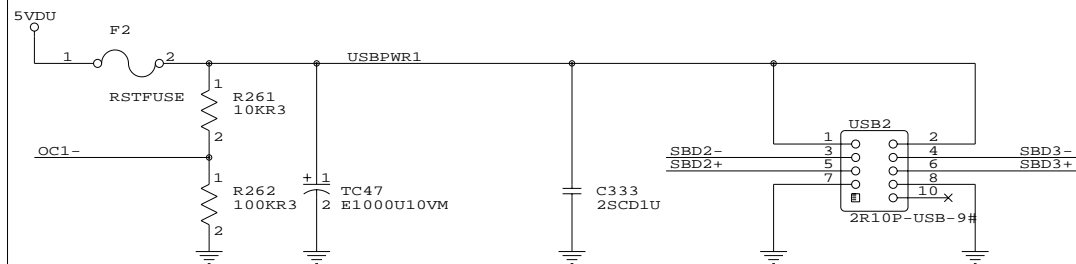


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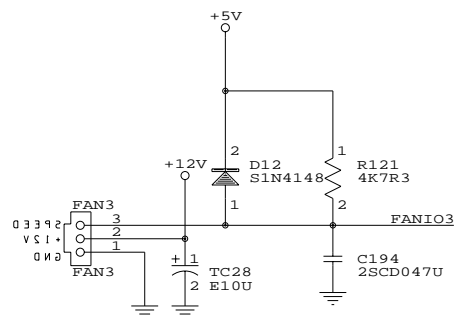
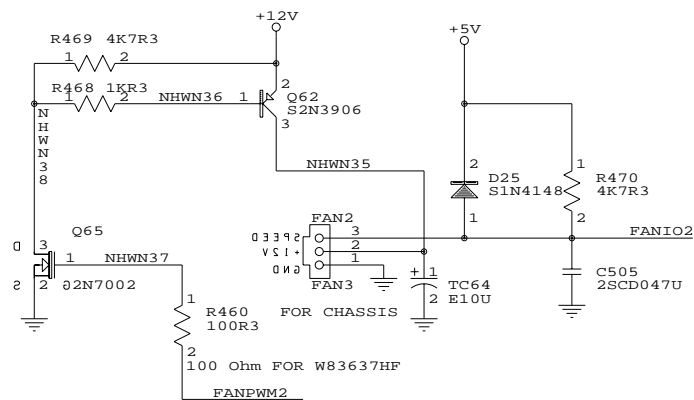
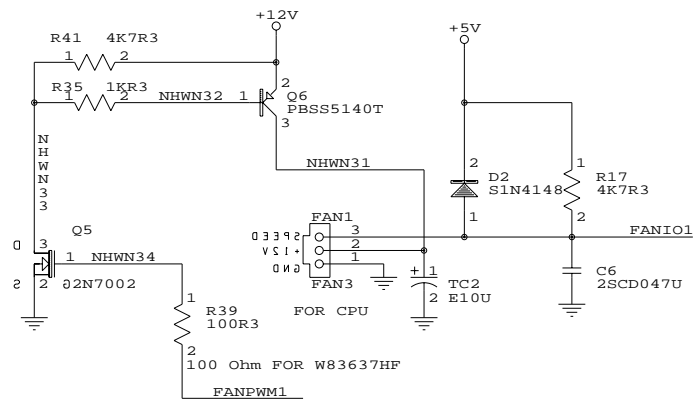


Albatron corp.		
Title RAID CONNECTOR		
Size	Document Number	REV
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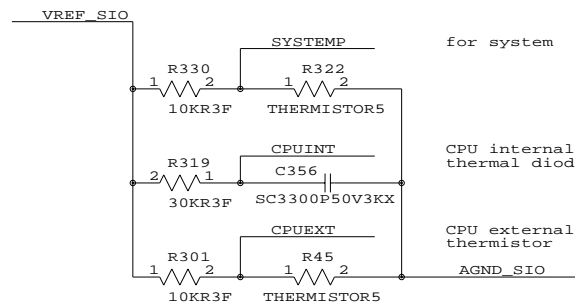
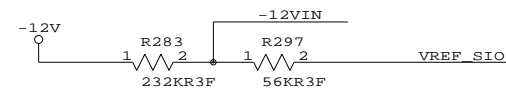
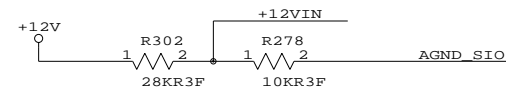
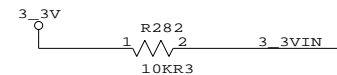
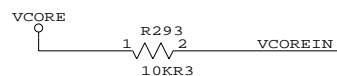
Albatron Corp.		
Title		
USB HEADER (PORT:4,5,6,7)		
Size	Document Number	REV
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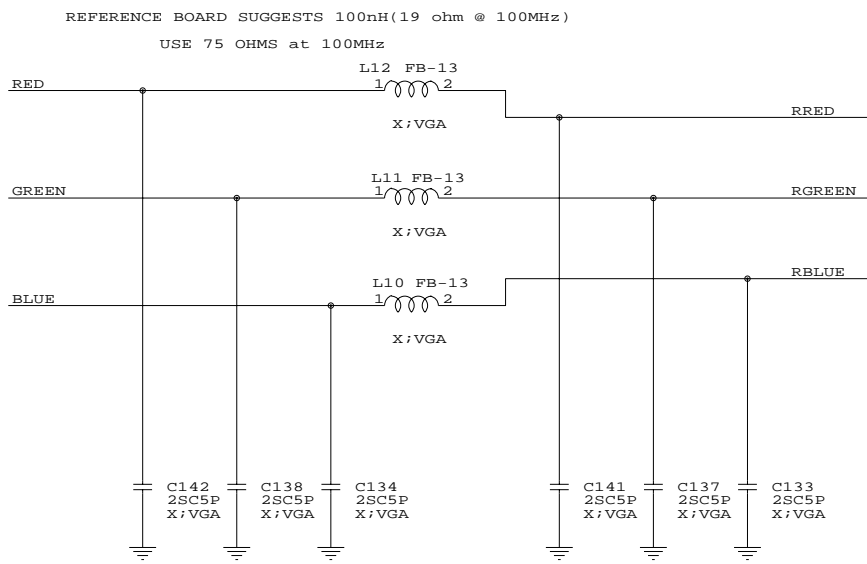
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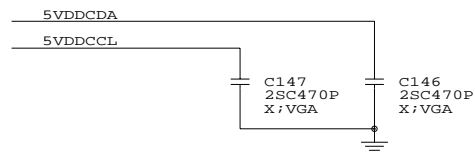
CPUINT	CPUINT	1, 37
SYSTEMP	SYSTEMP	37
CPUEXT	CPUEXT	37
FANIO1	FANIO1	37
FANIO2	FANIO2	37
FANIO3	FANIO3	37
FANPWM1	FANPWM1	37
FANPWM2	FANPWM2	37

VREF_SIO	VREF_SIO	37
AGND_SIO	AGND_SIO	1, 37
VCOREIN	VCOREIN	37
3_3VIN	3_3VIN	37
+12VIN	+12VIN	37
-12VIN	-12VIN	37

Albatron Corp.		
Title		
HARDWARE MONITOR		
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All these components should be placed
near by VGA connector

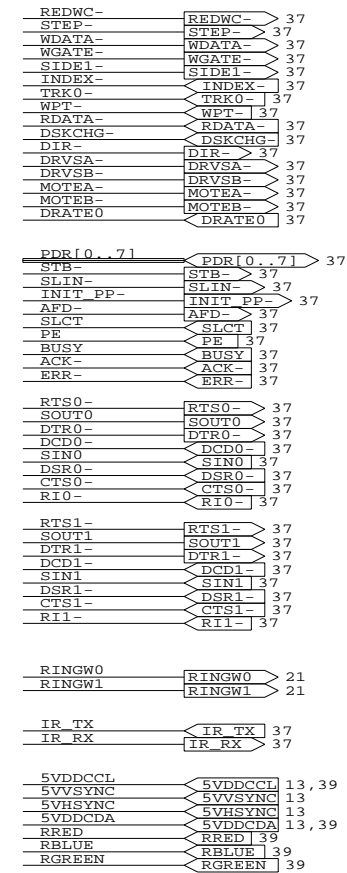
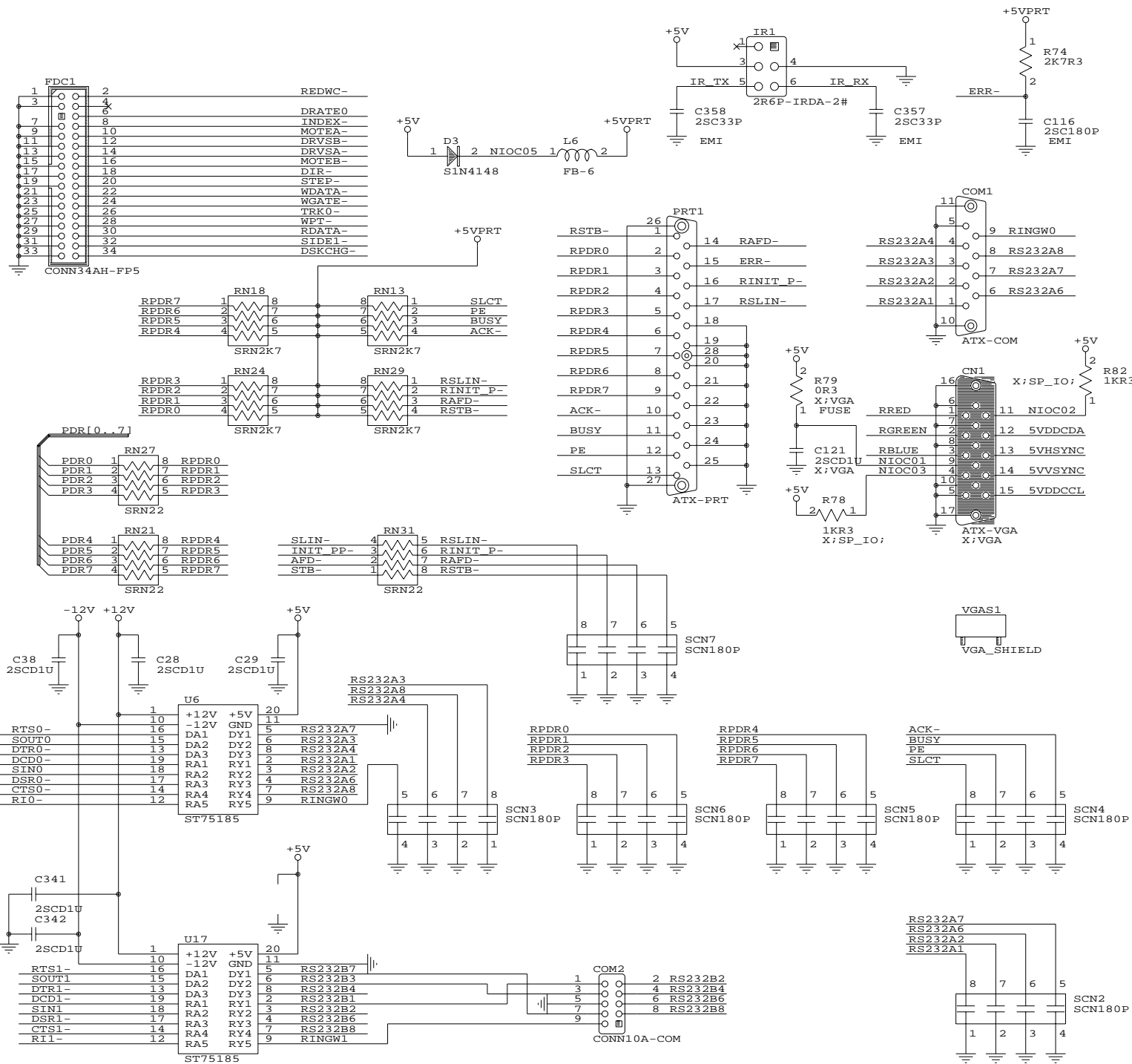


RED → RED 13
GREEN → GREEN 13
BLUE → BLUE 13

5VDDCCL → 5VDDCCL 13,40
5VDDCDA → 5VDDCDA 13,40
RRED → RRED 40
RBLUE → RBLUE 40
RGREEN → RGREEN 40

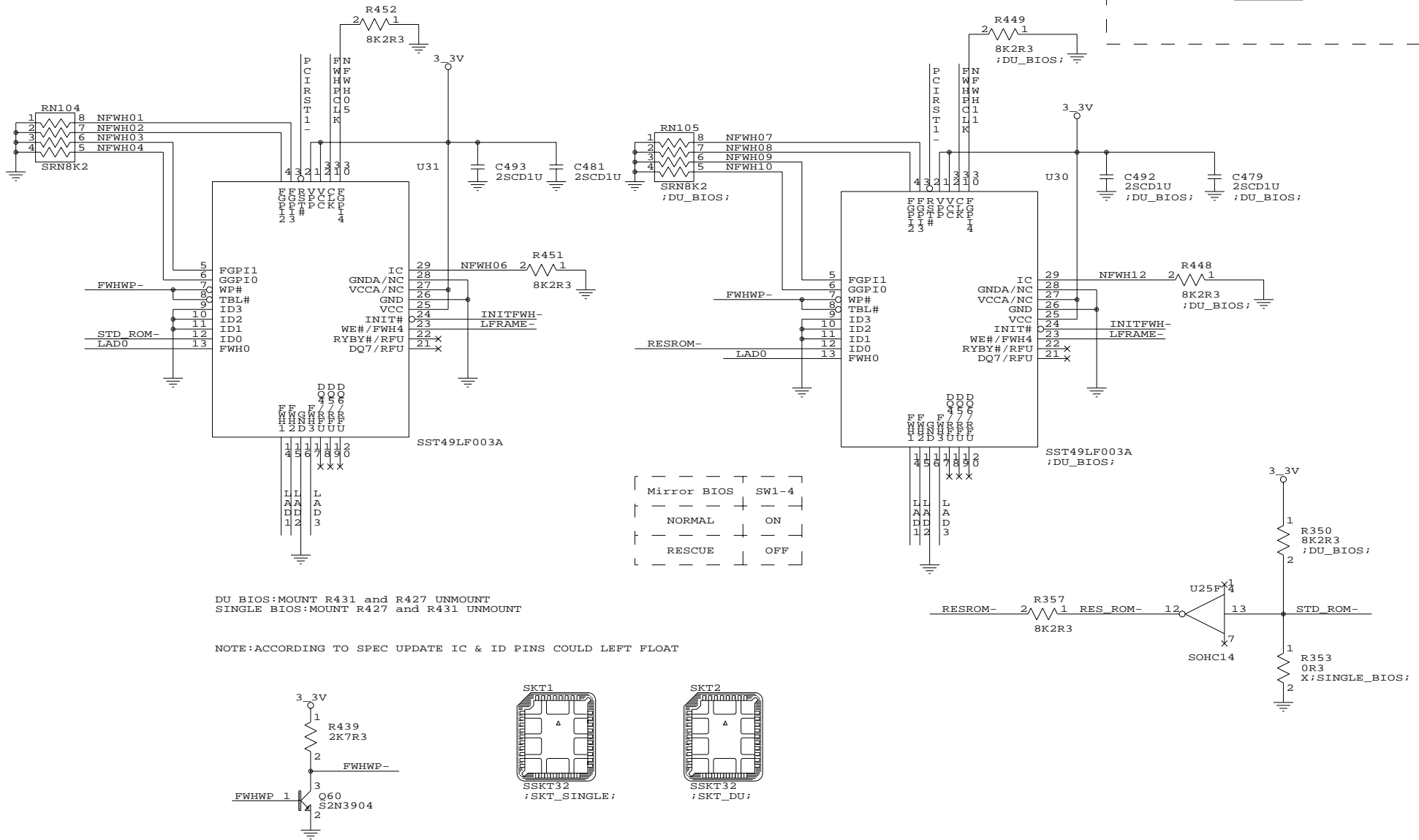
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Albatron Corp.		
Title		
VGA CONNECTOR		
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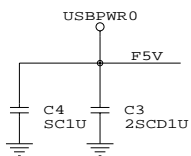
Albatron Corp.		
Title	I/O CONNECTOR	
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DU BIOS:MOUNT R431 and R427 UNMOUNT
SINGLE BIOS:MOUNT R427 and R431 UNMOUNT

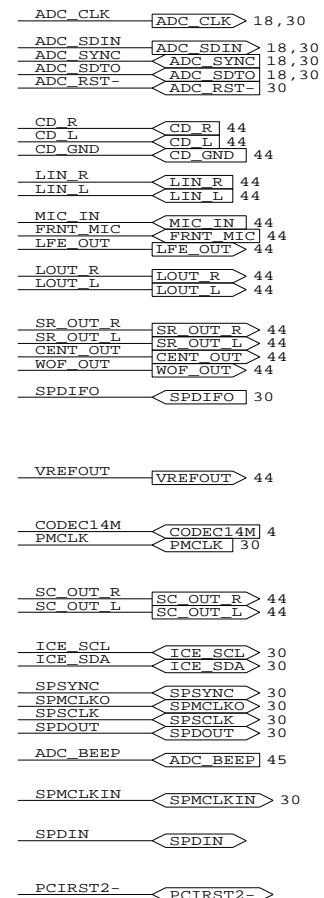
NOTE: ACCORDING TO SPEC UPDATE IC & ID PINS COULD LEFT FLOAT

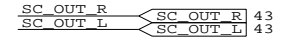
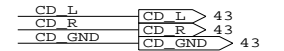
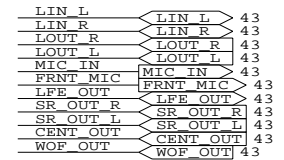
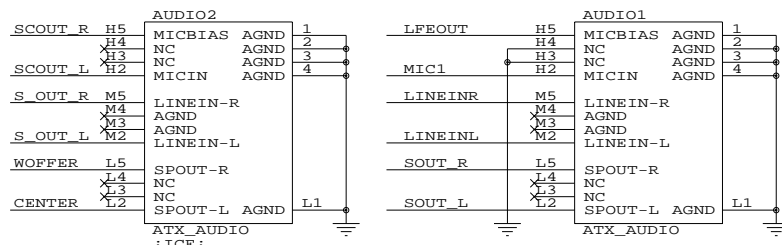
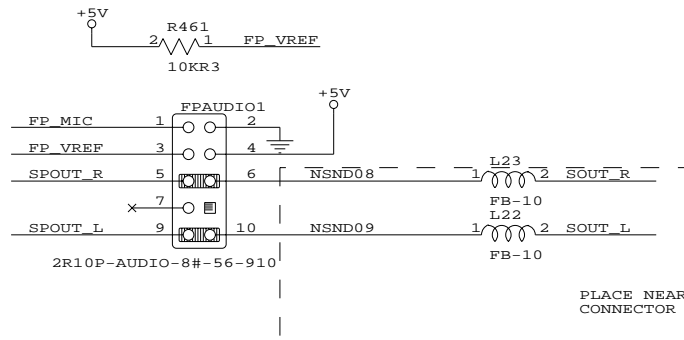
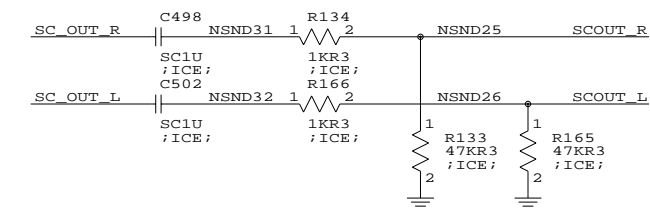
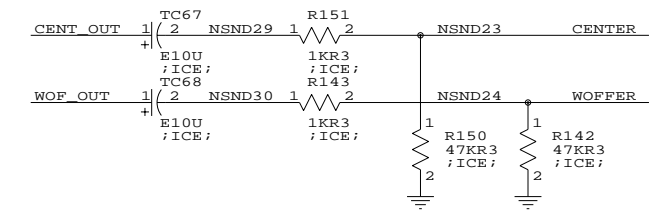
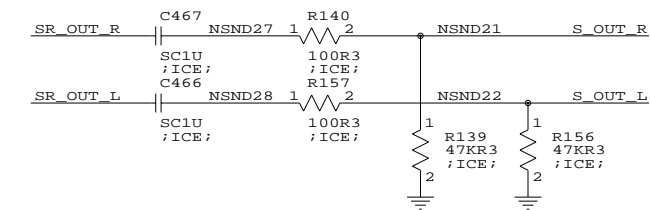
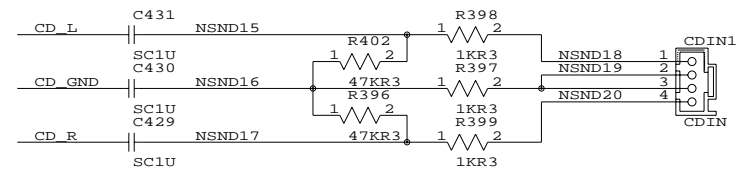
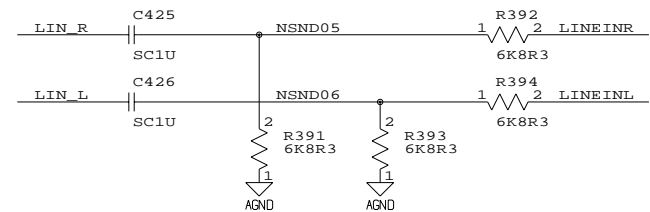
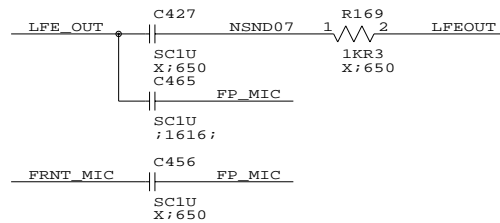
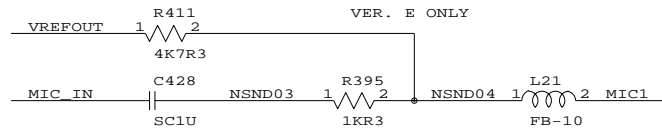
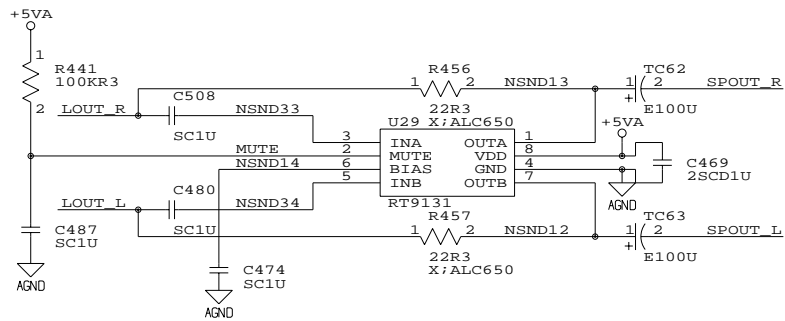
LAD[0..3] LAD[0..3] 19,37
LFRAME- LFRAME- 19
INITFWH- INITFWH- 21
FWHPCLK FWHPCLK 4
PCIRST1- PCIRST1- 13,21
FWHWP FWHWP 19
STD_ROM- STD_ROM- 45



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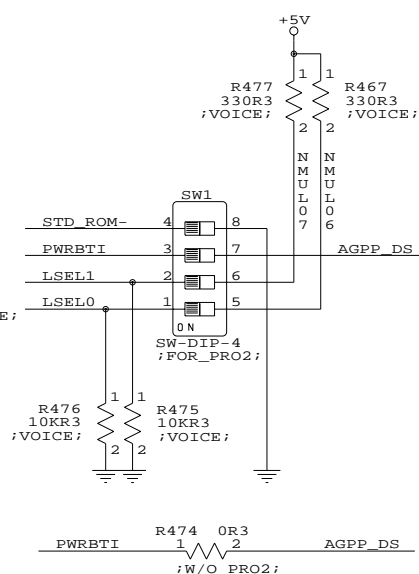
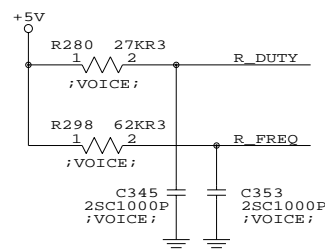
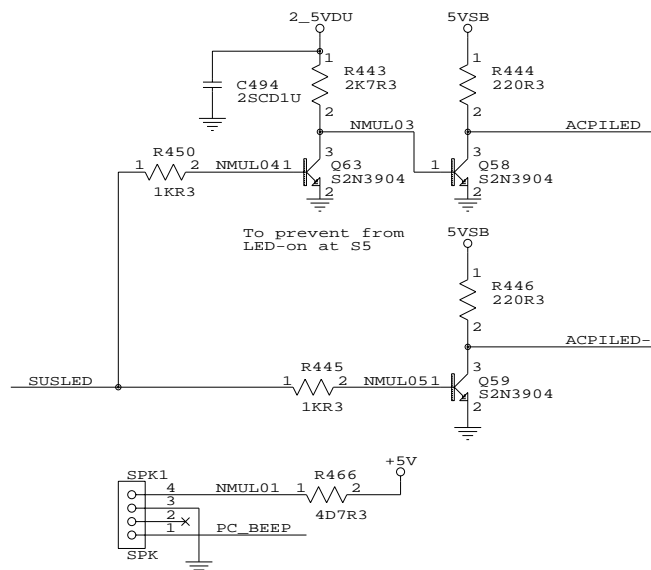
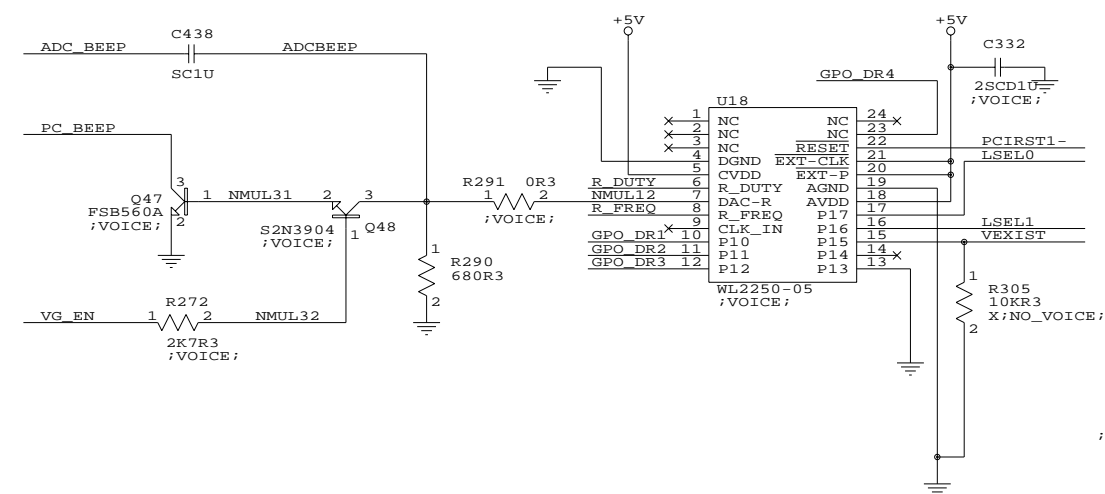
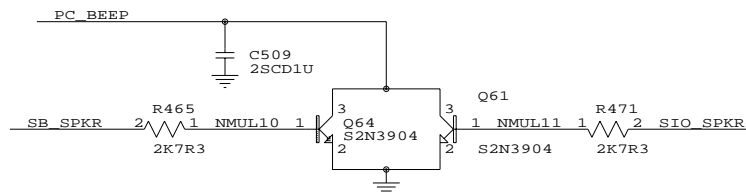




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Albatron Corp.		
Title		
SND_CONN		
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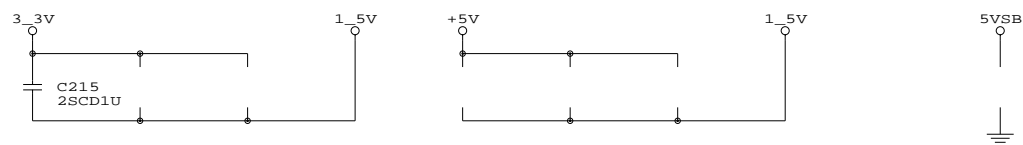
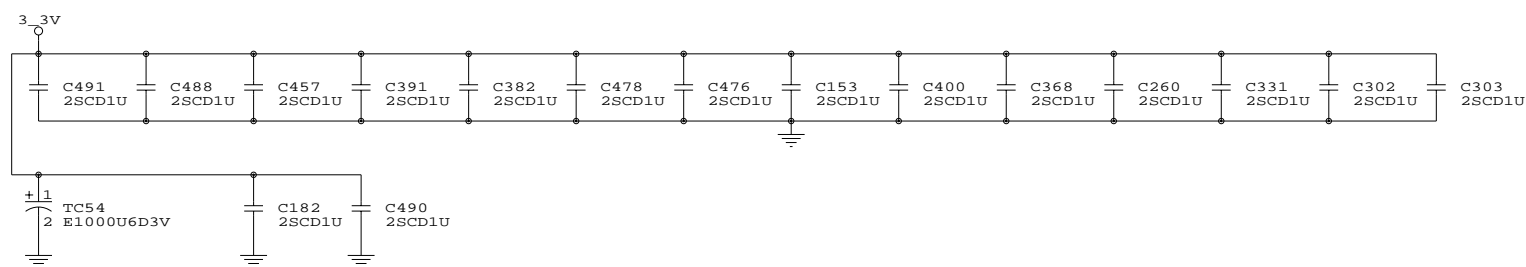
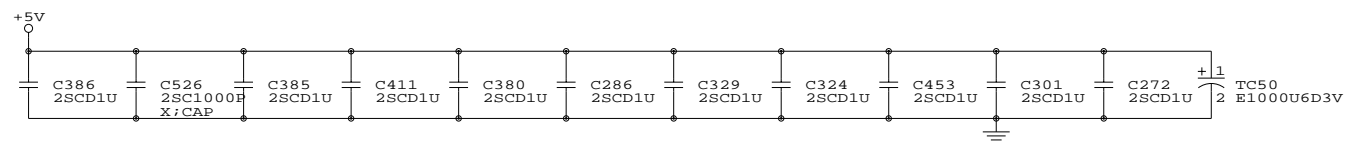
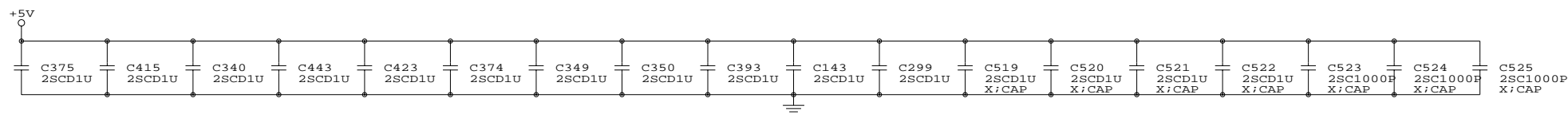
HD LED	HD_LED_P	1	2	FP PWR/SLP	ACPI LED
	HD_LED_N	3	4	FP PWR/SLP	
RST SW	RST_SW_N	5	6	PWR_SW_P	PWR SW
	RET_SW_P	7	8	PWR_SW_N	
	RSVD_DNU	9	10	NC	



Voice Genie	SW1-1	SW1-2
ENGLISH	ON	ON
CHINESE	ON	OFF
JAPANESE	OFF	ON
GERMAN	OFF	OFF

AGP Protection	SW1-3
ENABLE	ON
DISABLE	OFF

Mirror BIOS	SW1-4
NORMAL	ON
RESCUE	OFF



Albatron Corp.		
Title		
CAP		
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